

FIG. 1

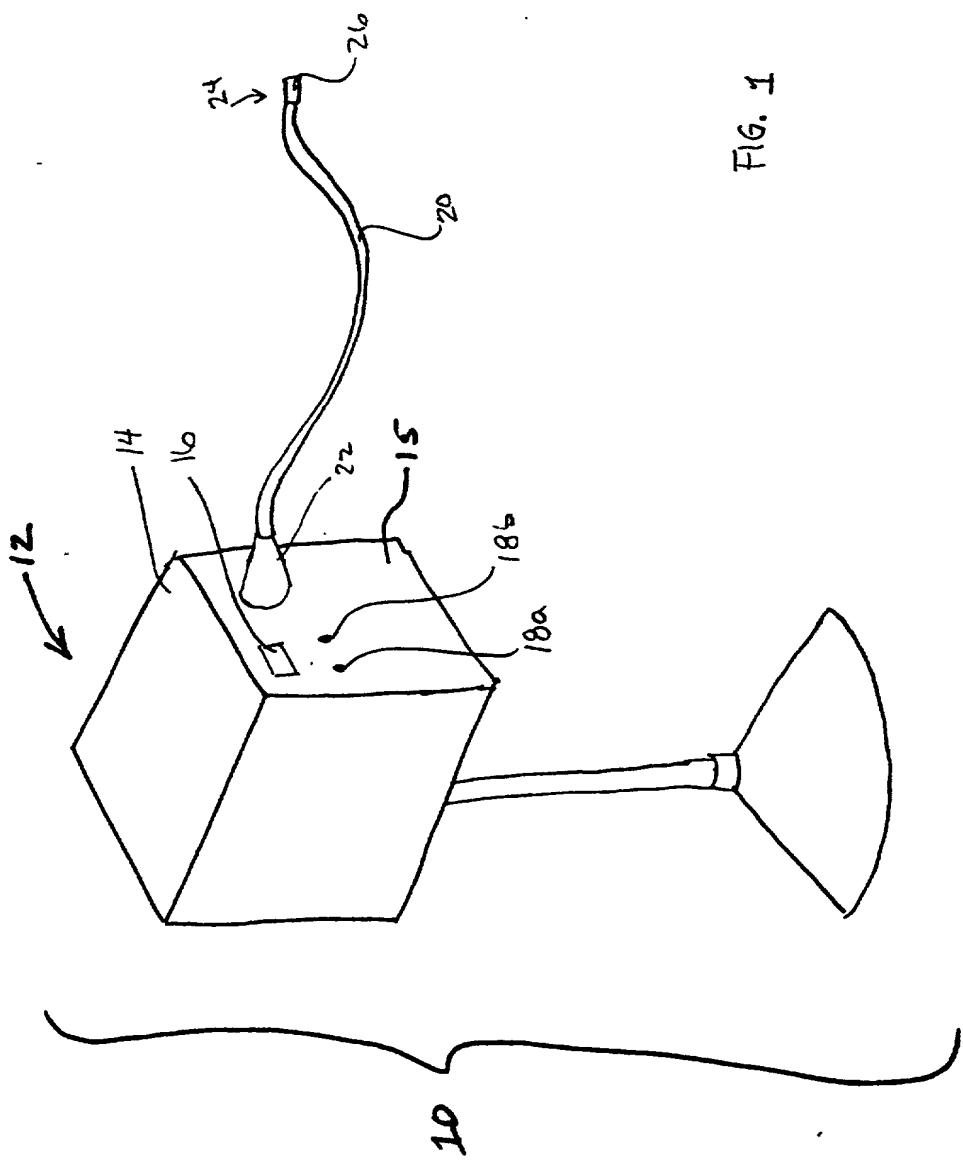
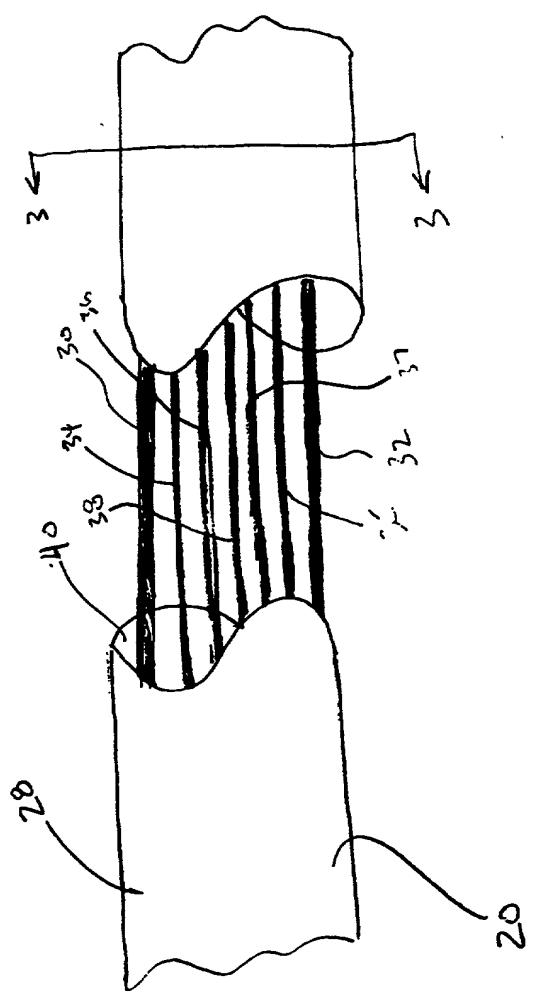


FIG. 2



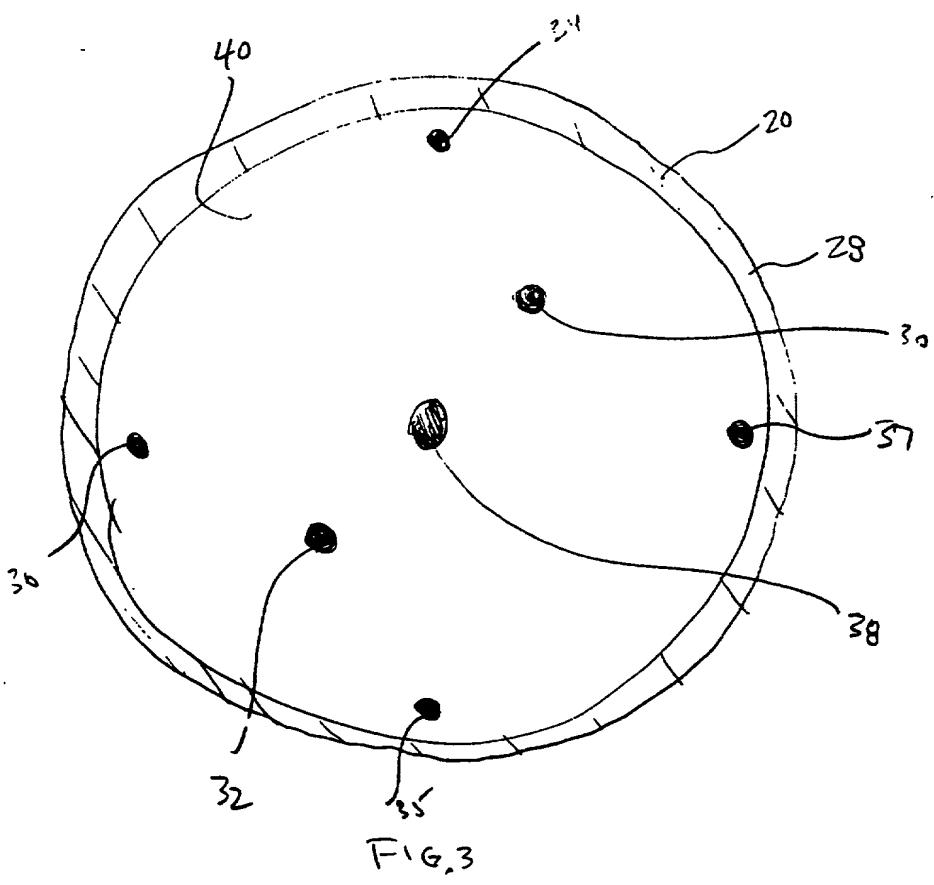
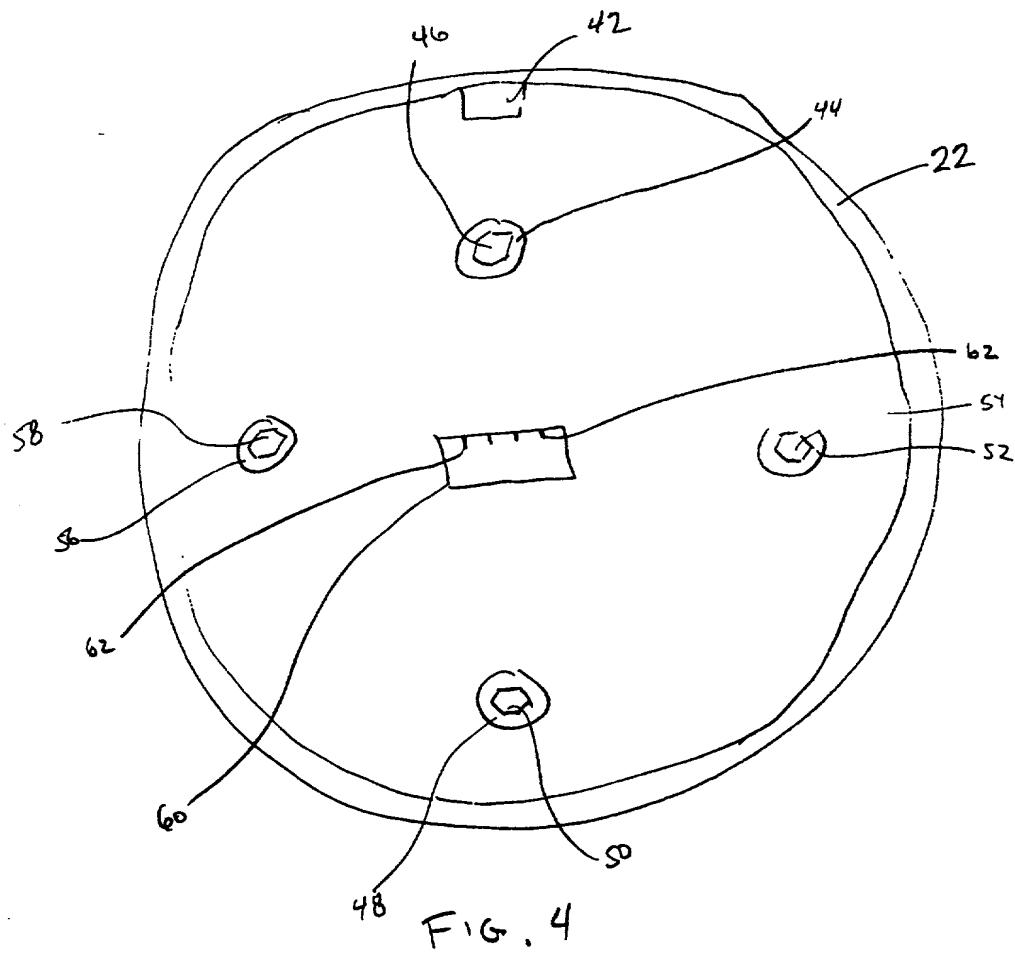


FIG. 3



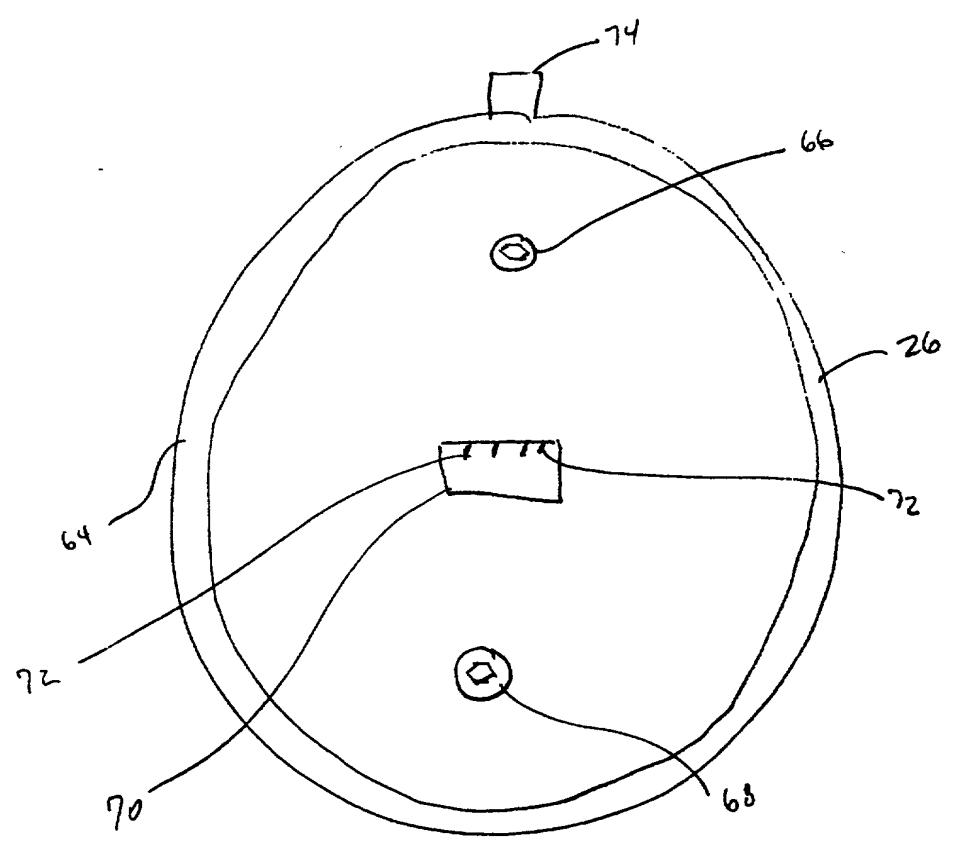


FIG. 5

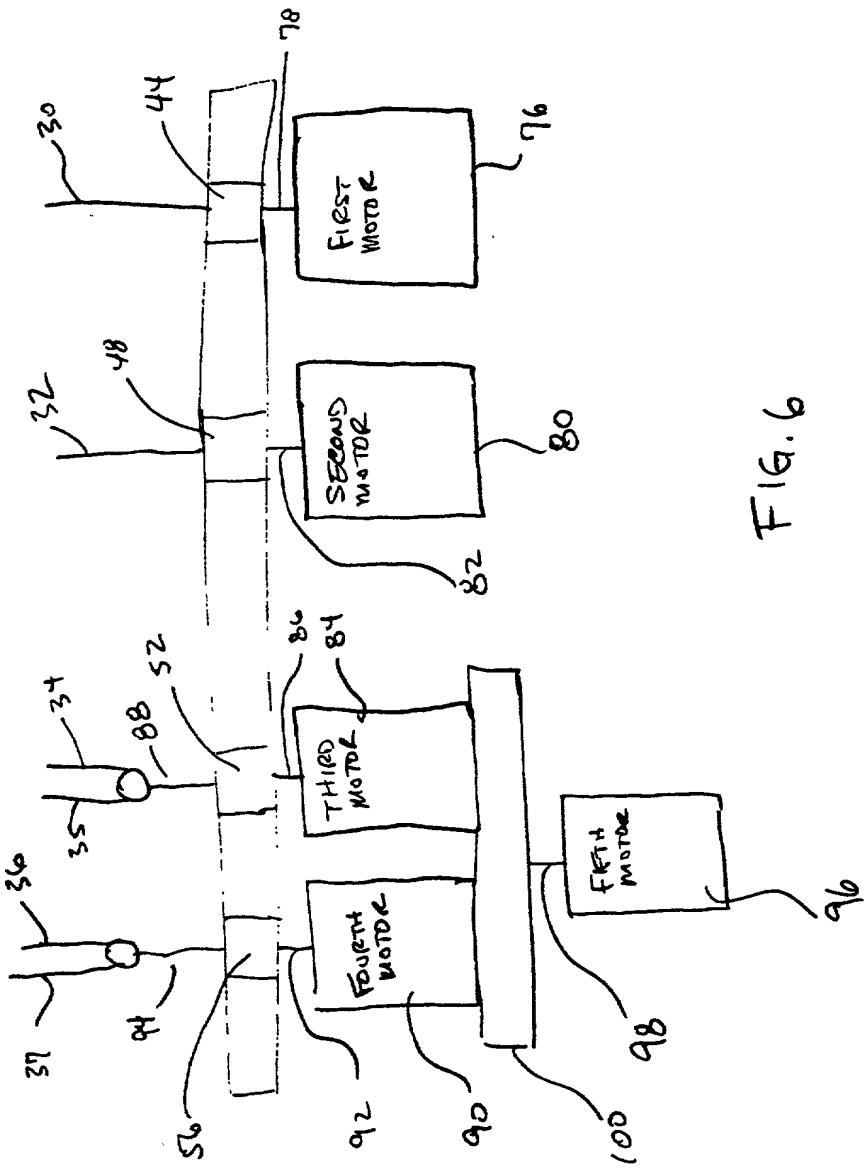
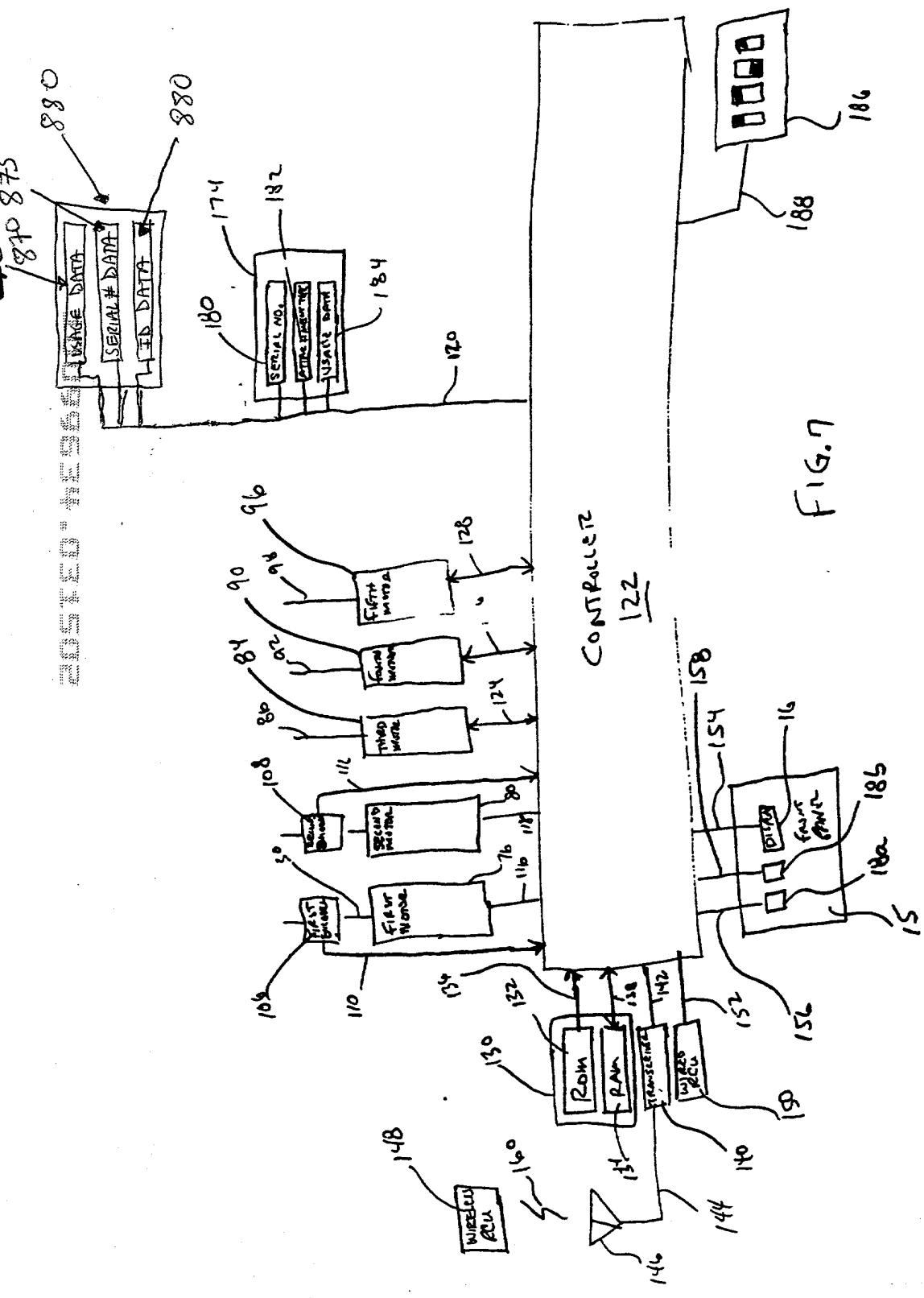


FIG. 6



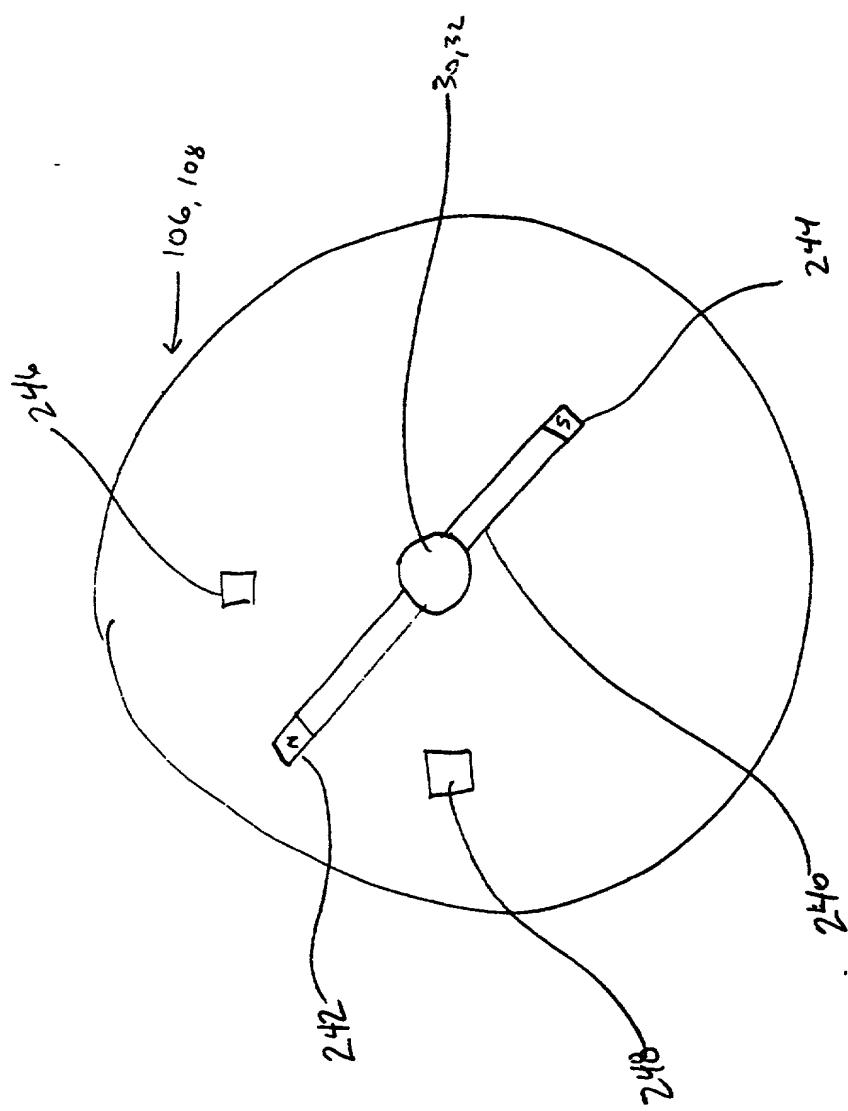


Fig. 8

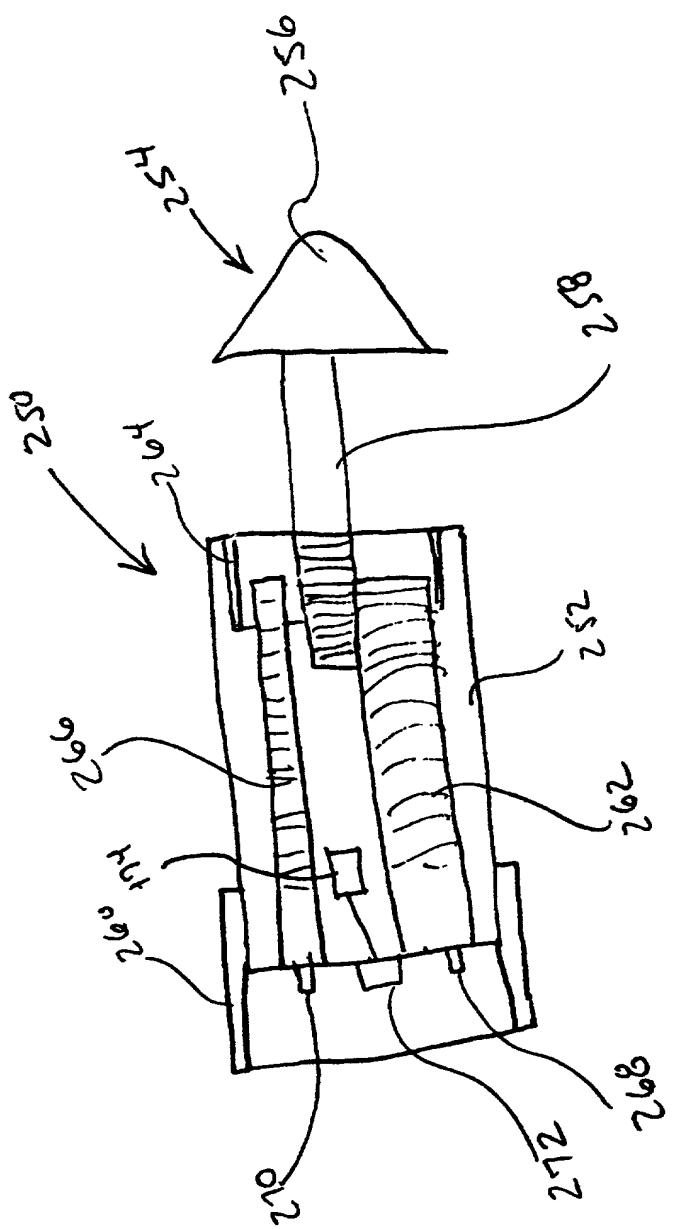
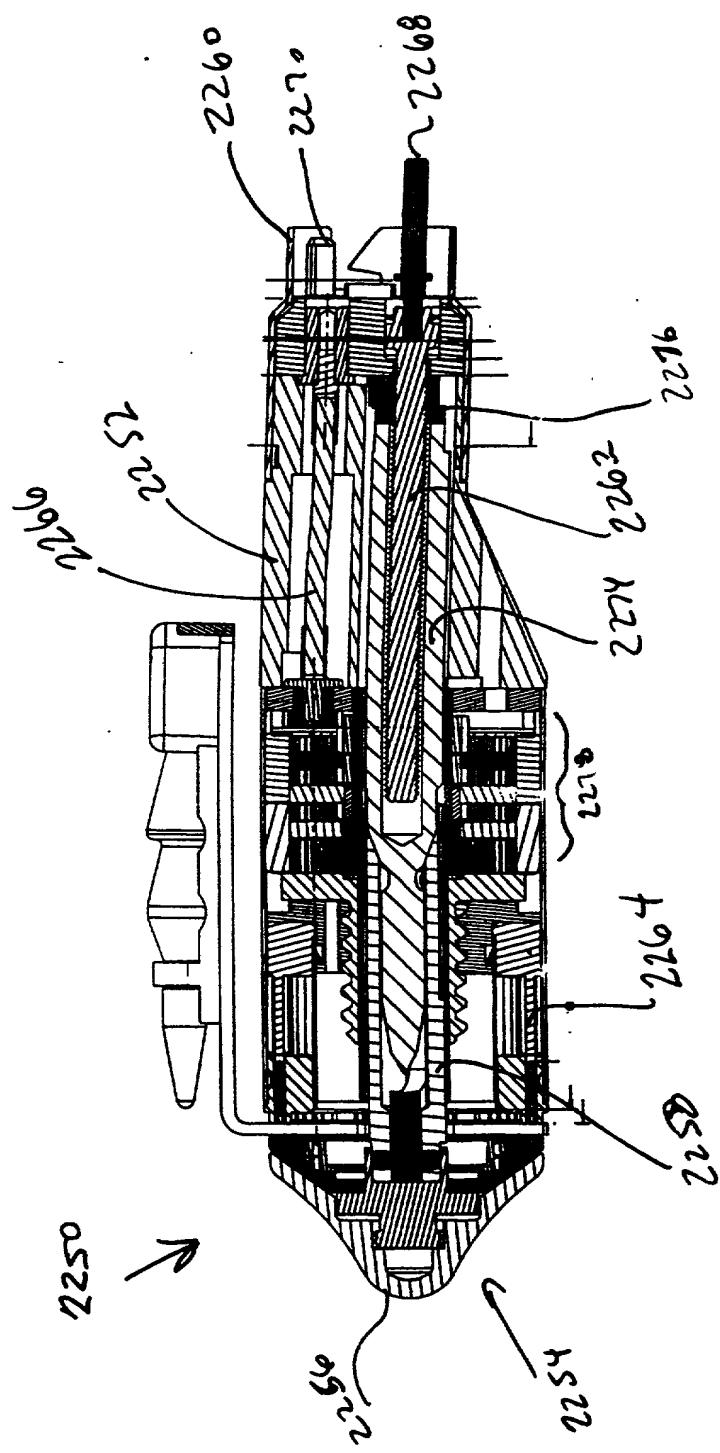


Fig. 9A

Figure 9B



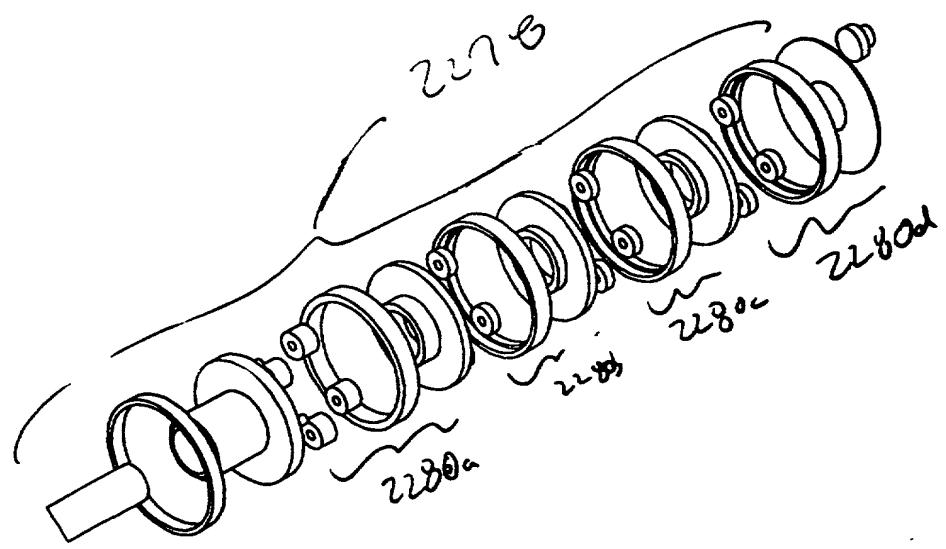
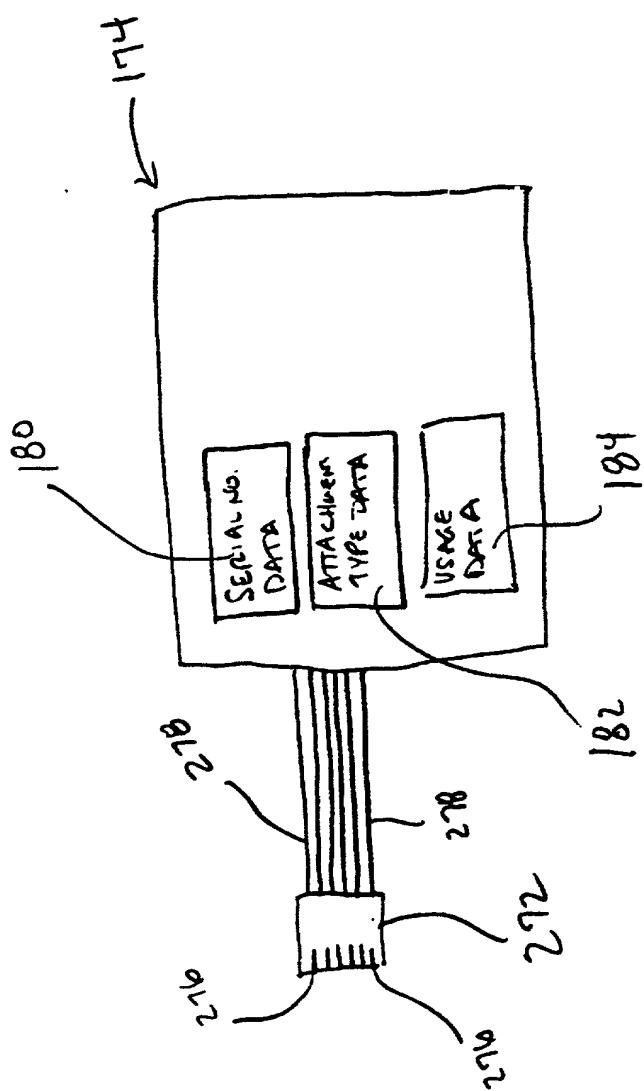
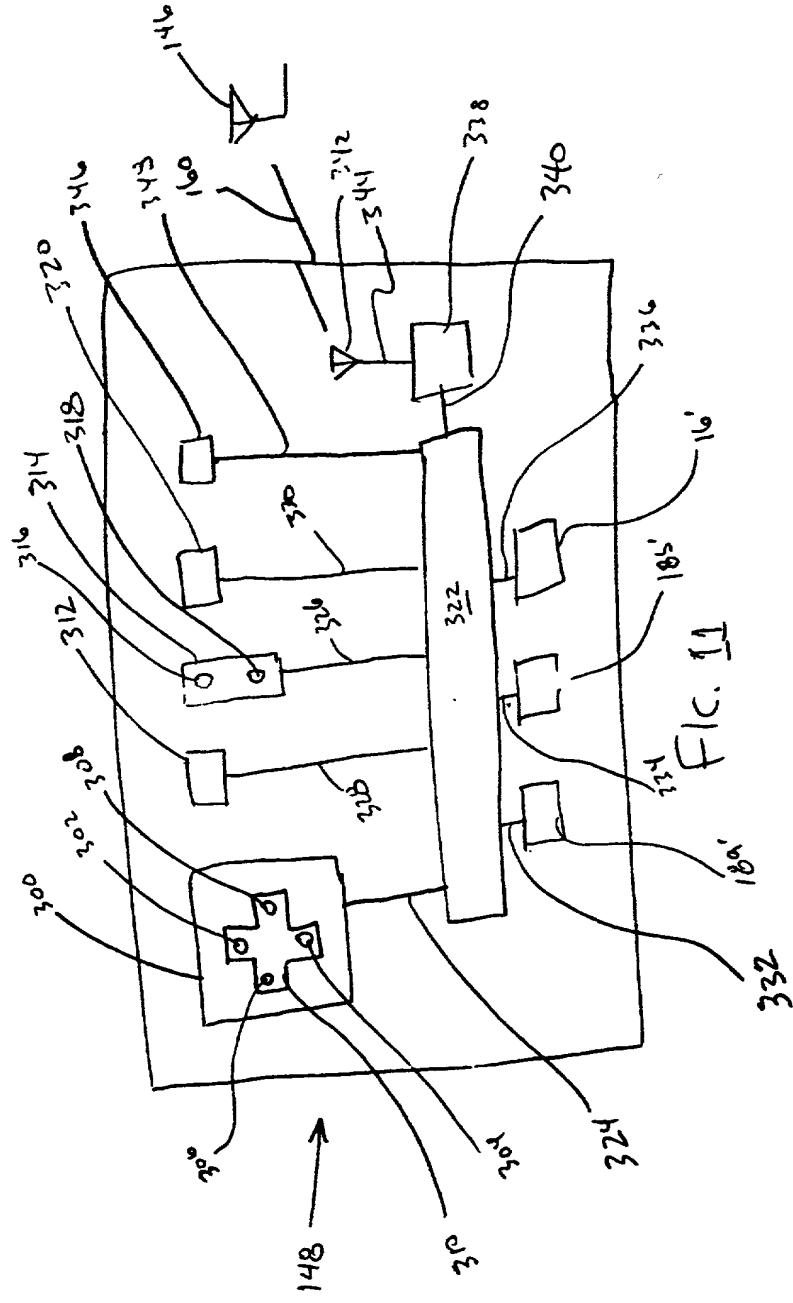


FIG. 9C

F.16. 10





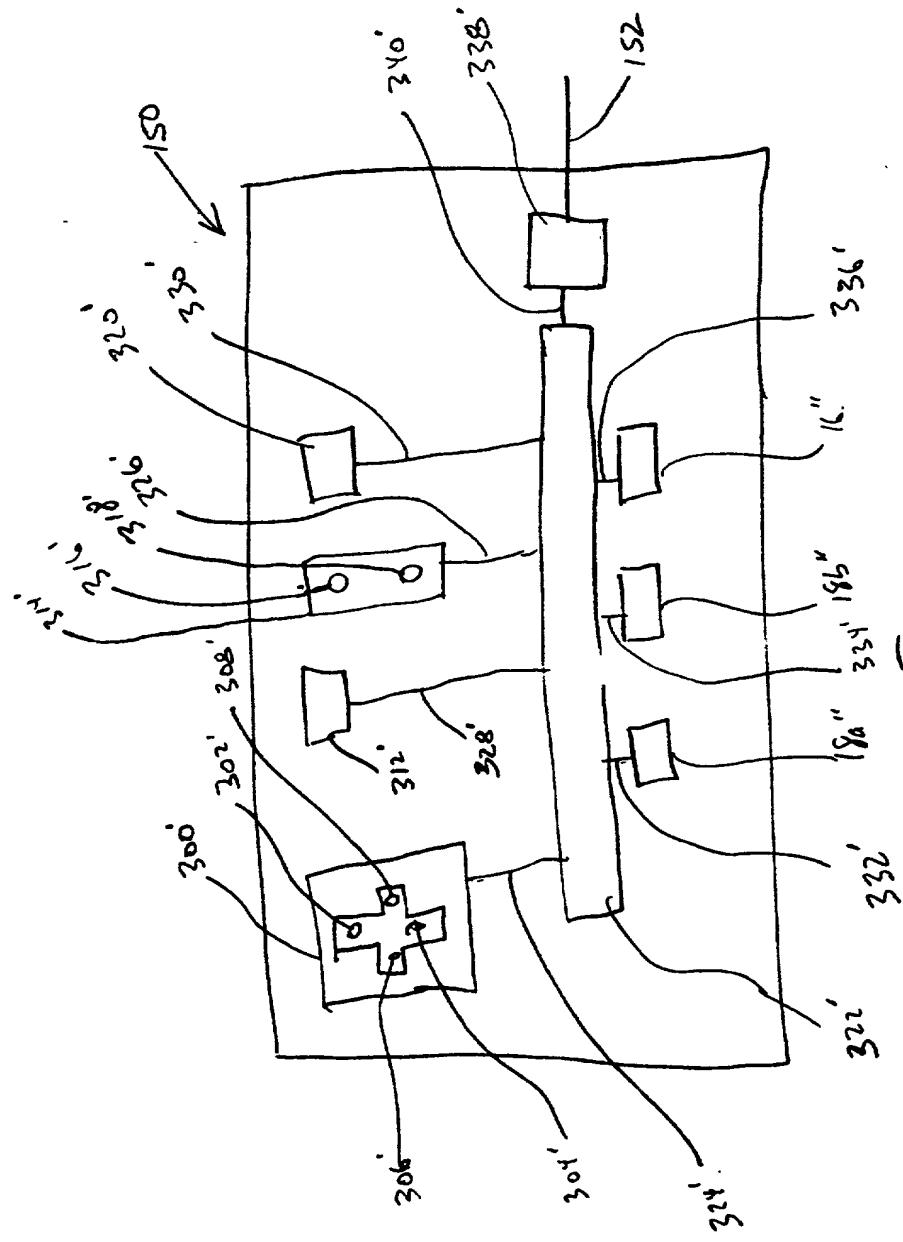


FIG. 12

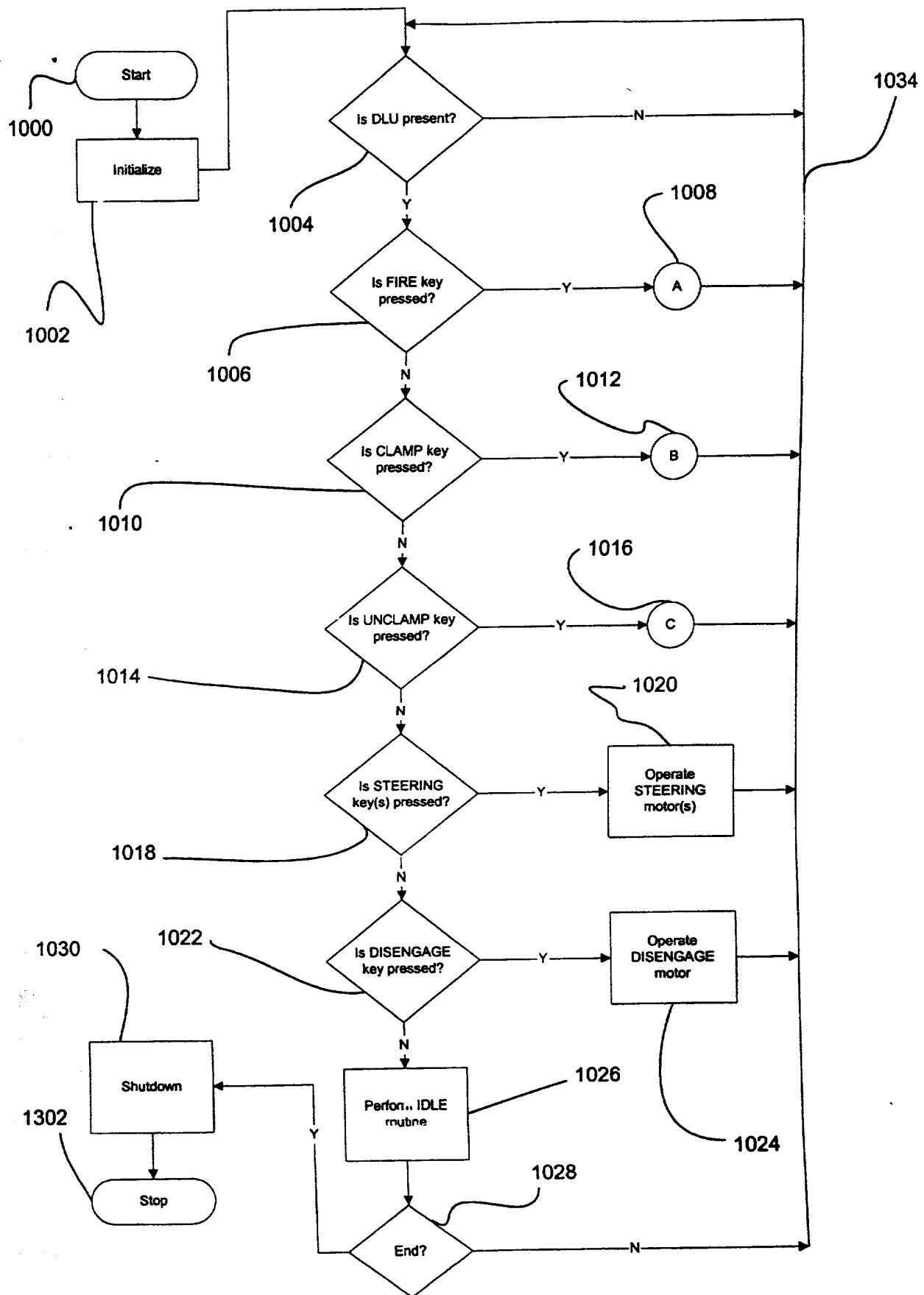


Figure 13

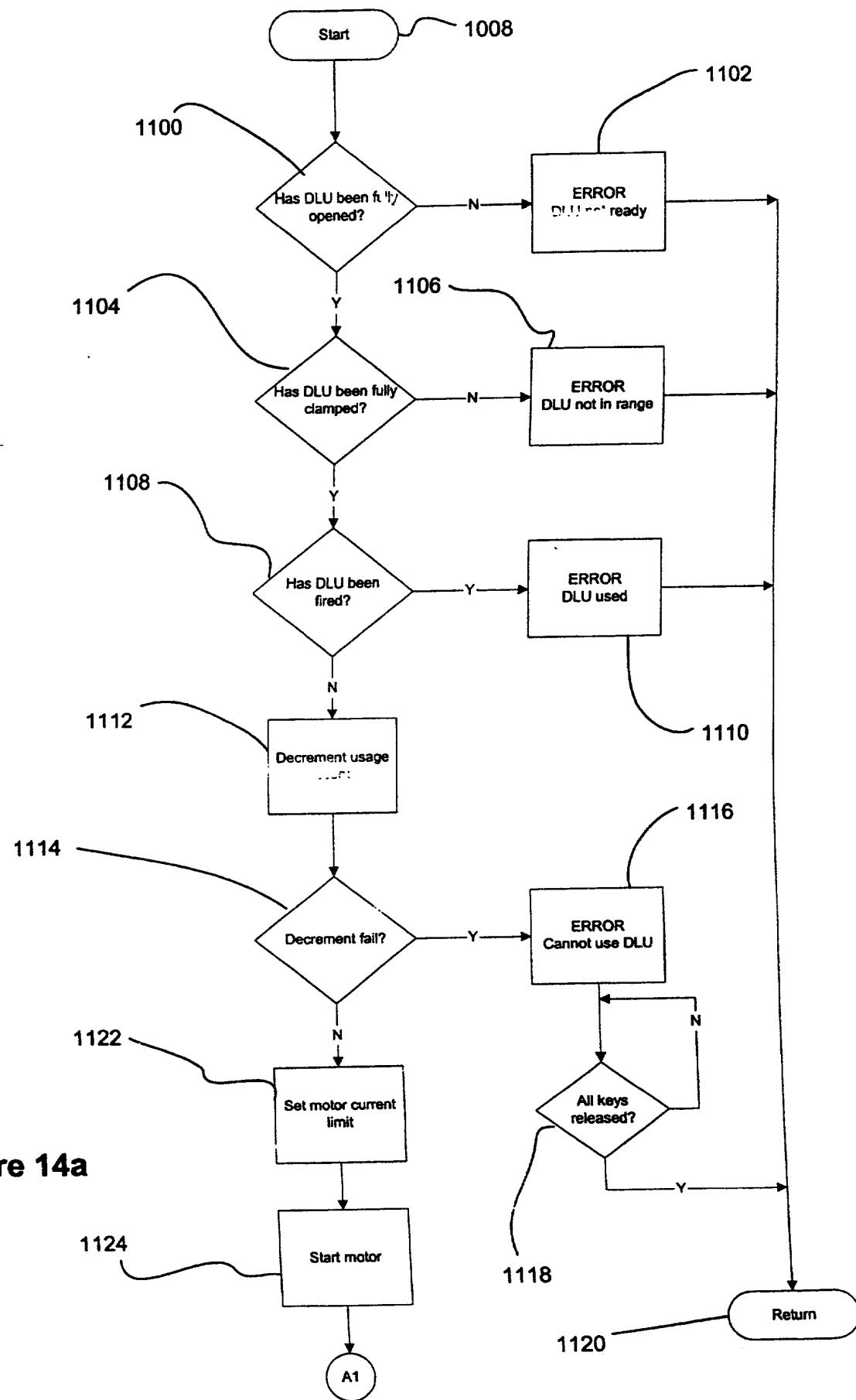


Figure 14a

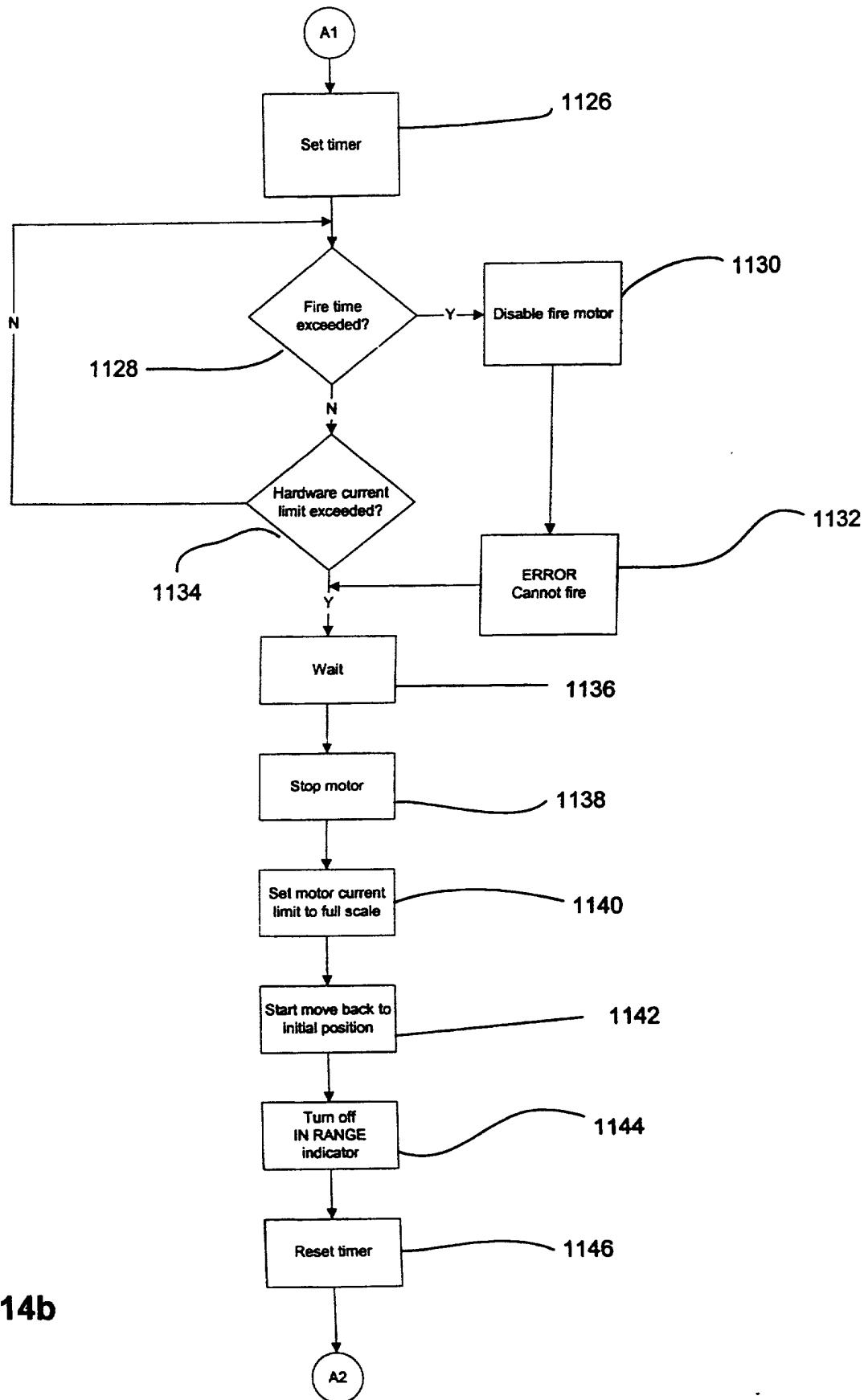


Figure 14b

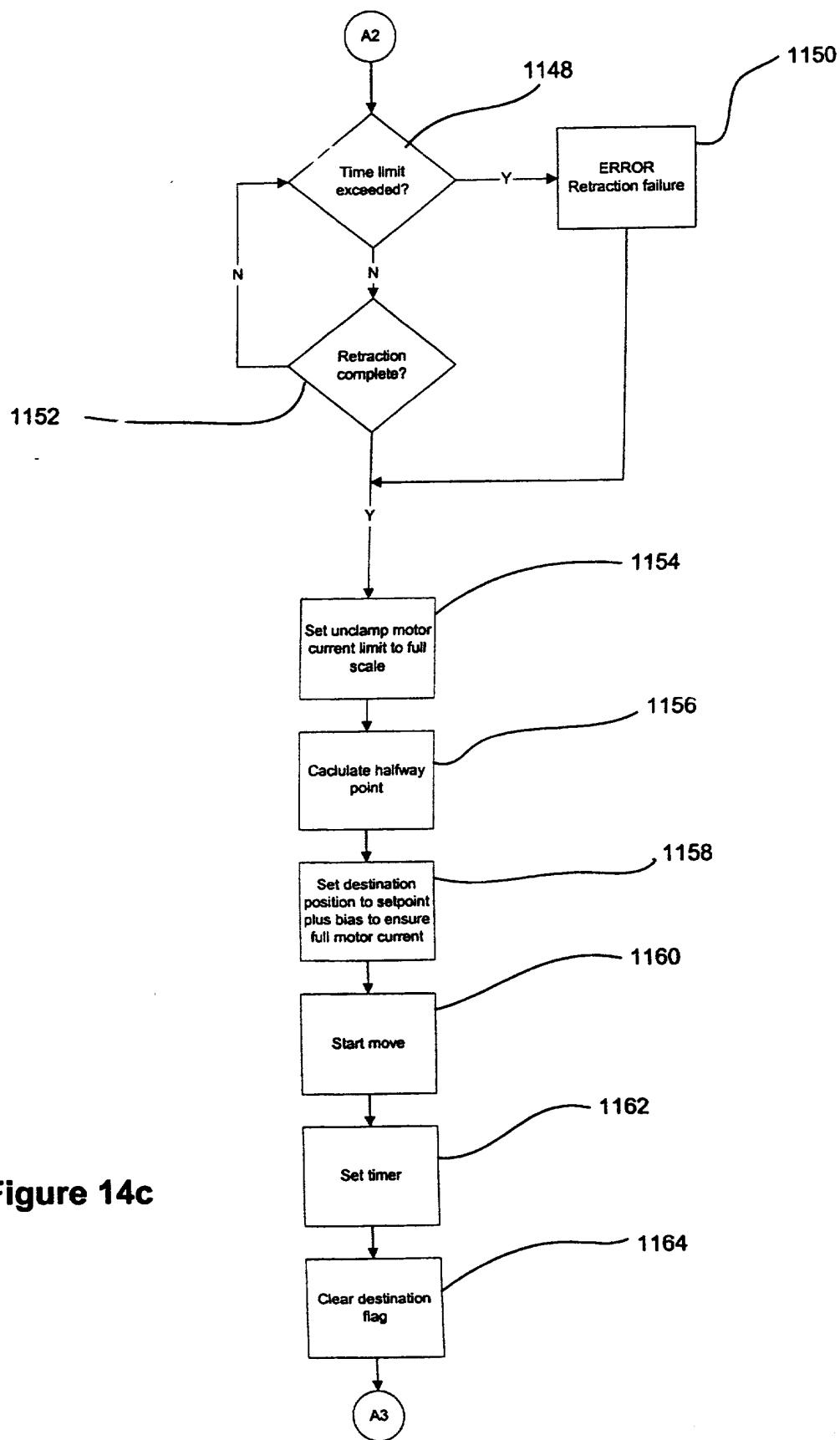


Figure 14c

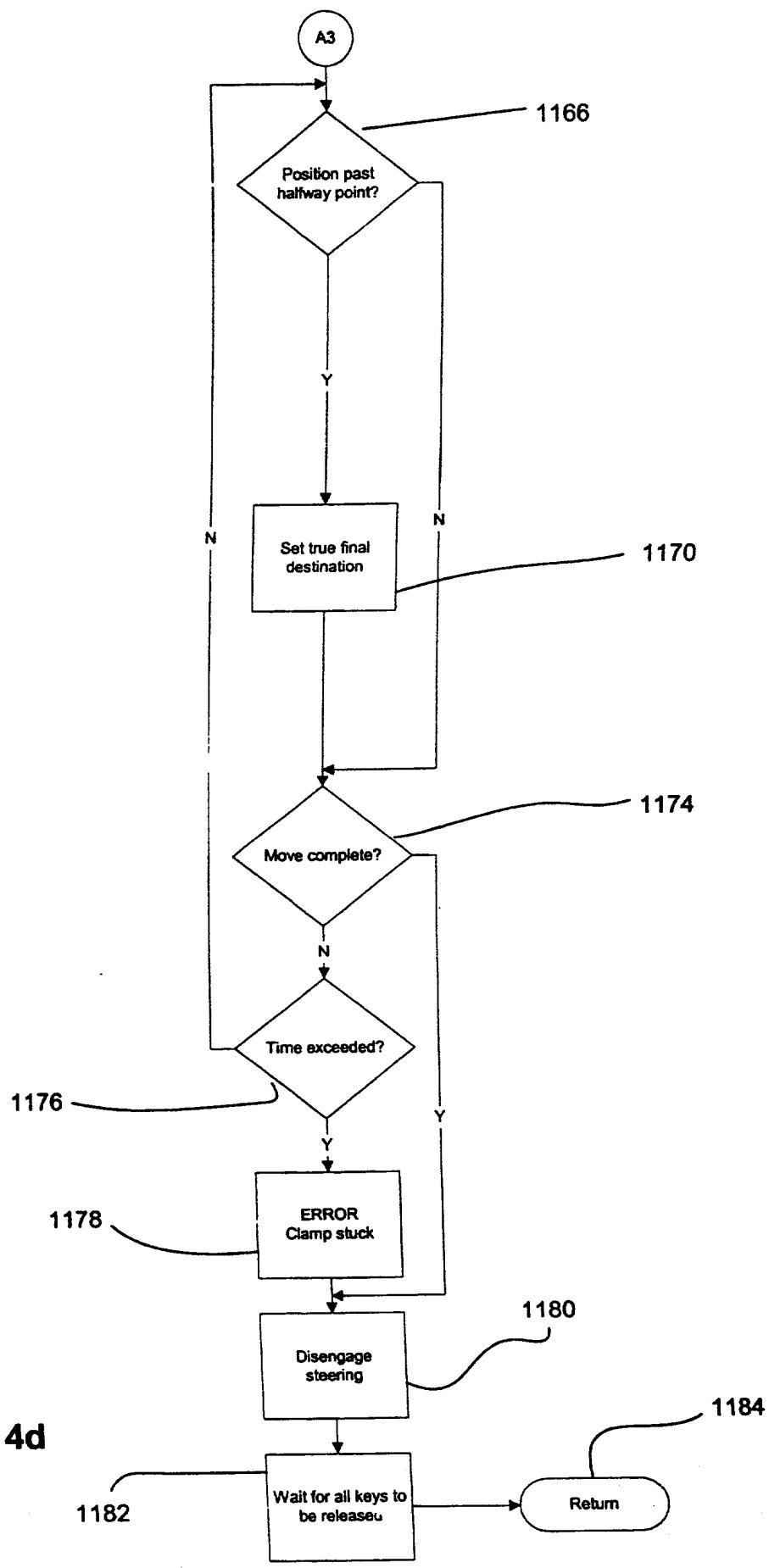


Figure 14d

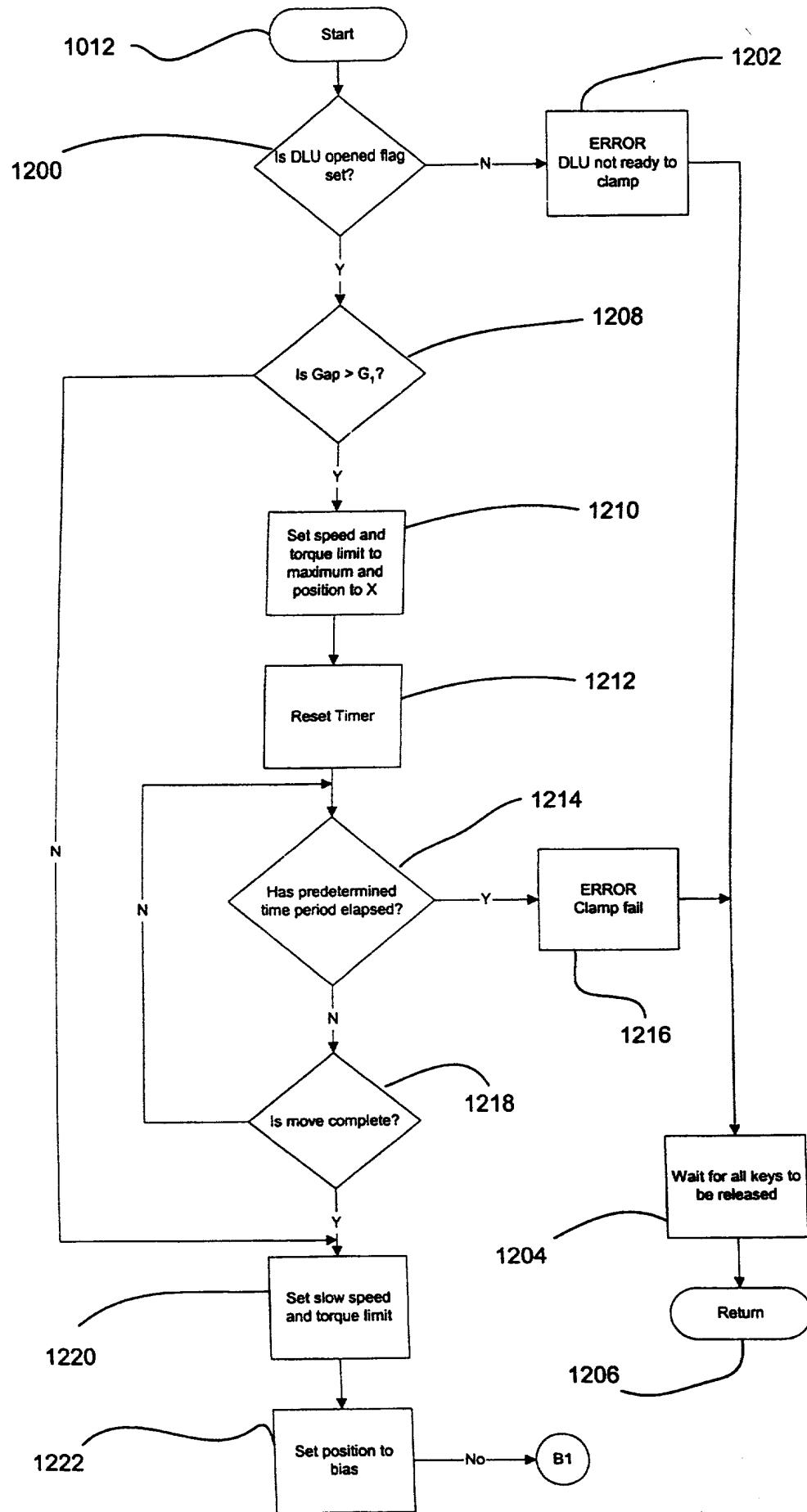


Figure 15a

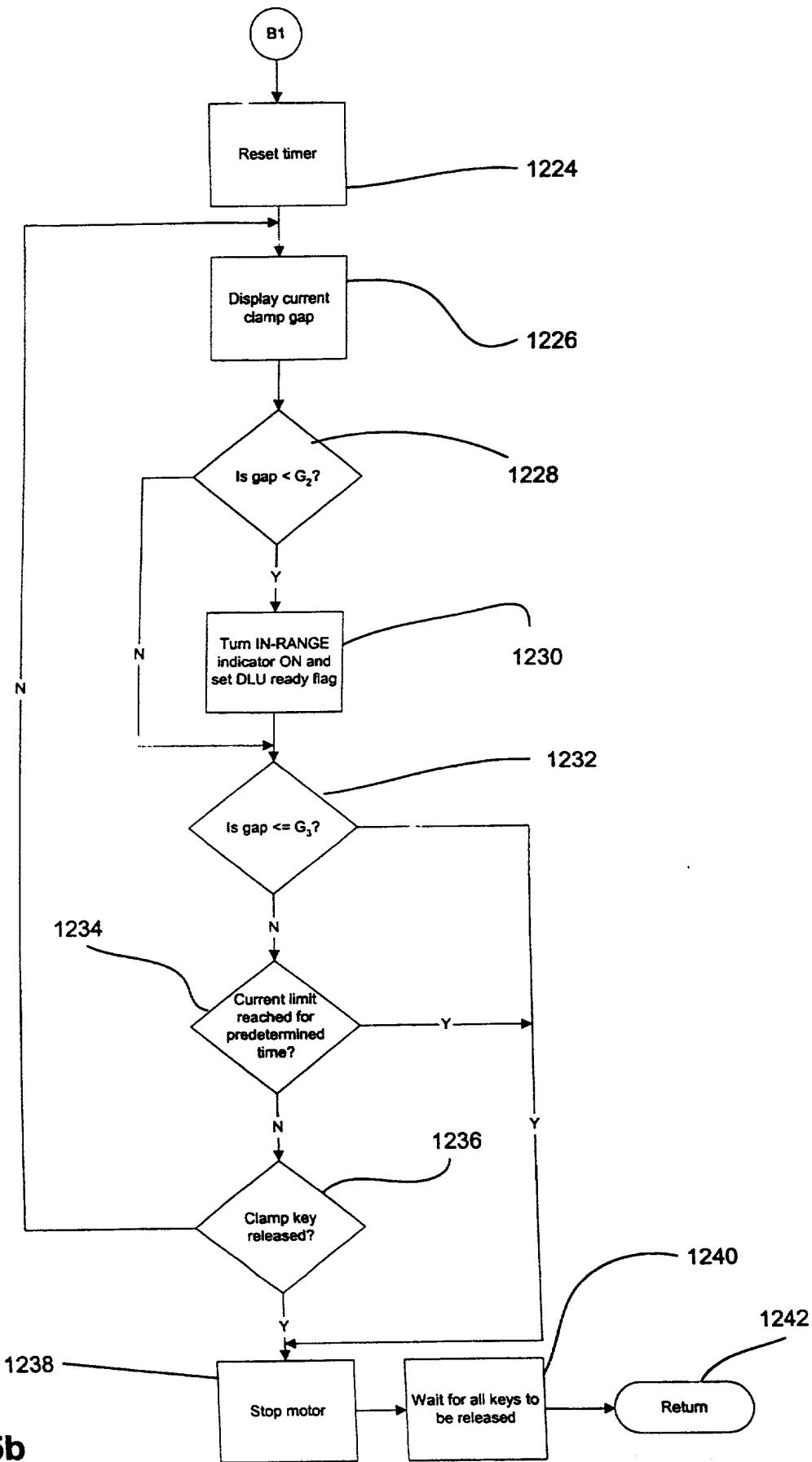


Figure 15b

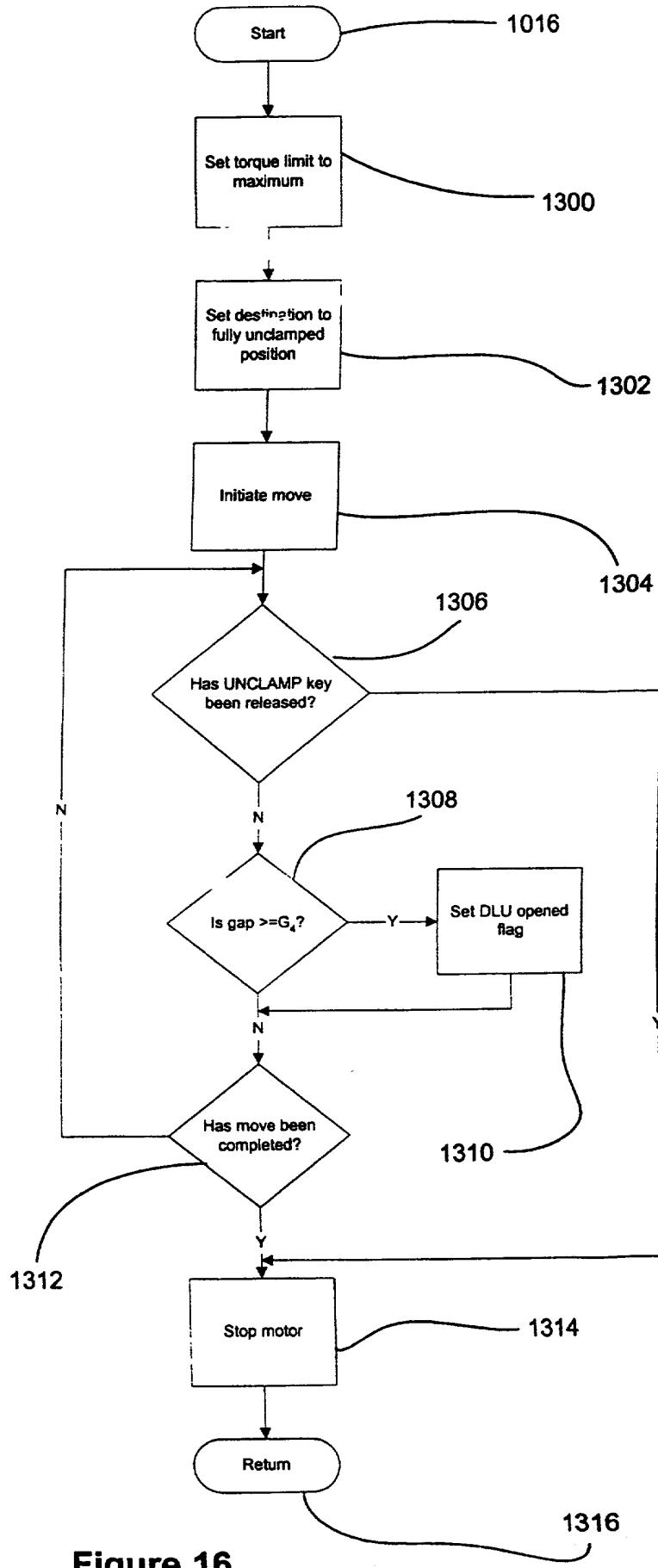


Figure 16

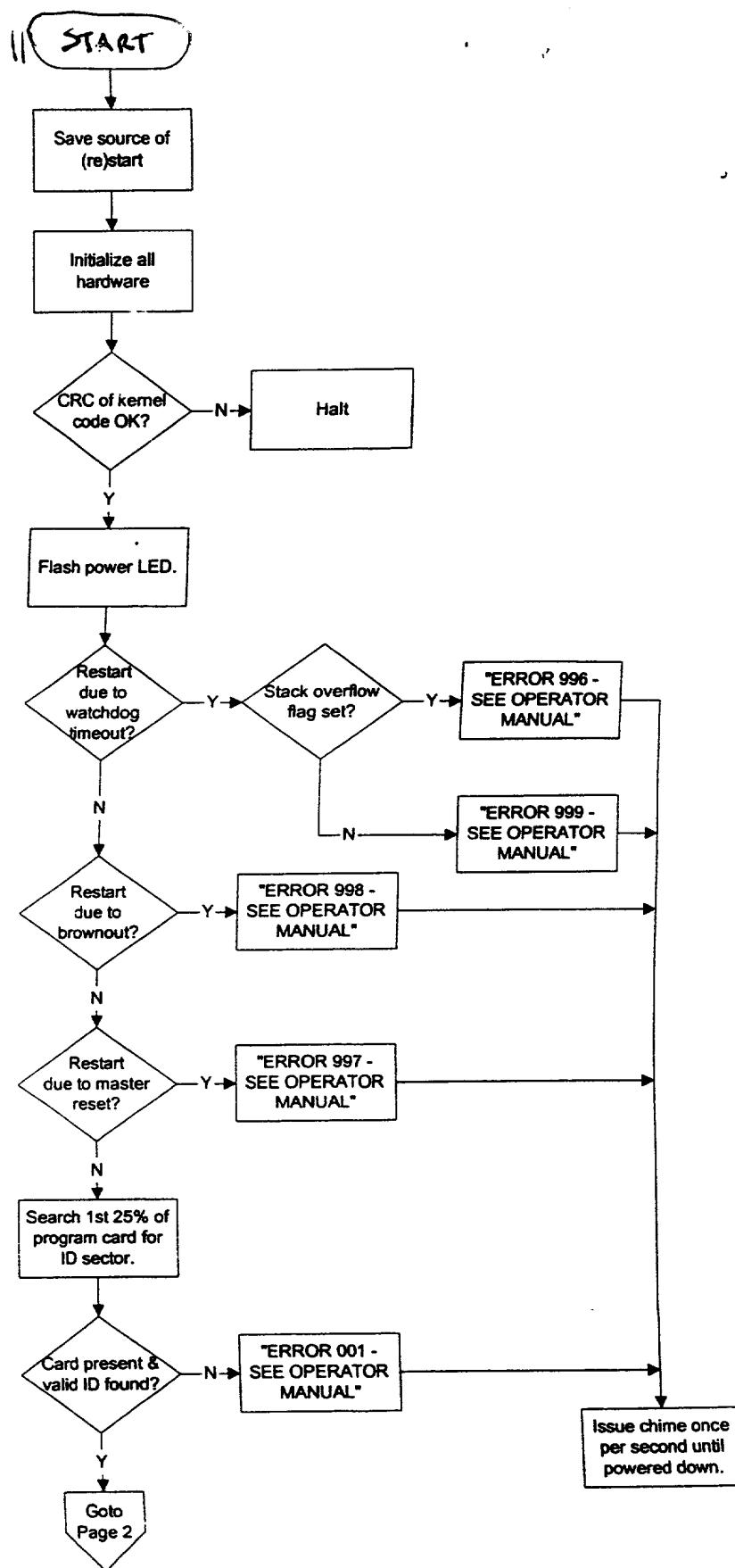
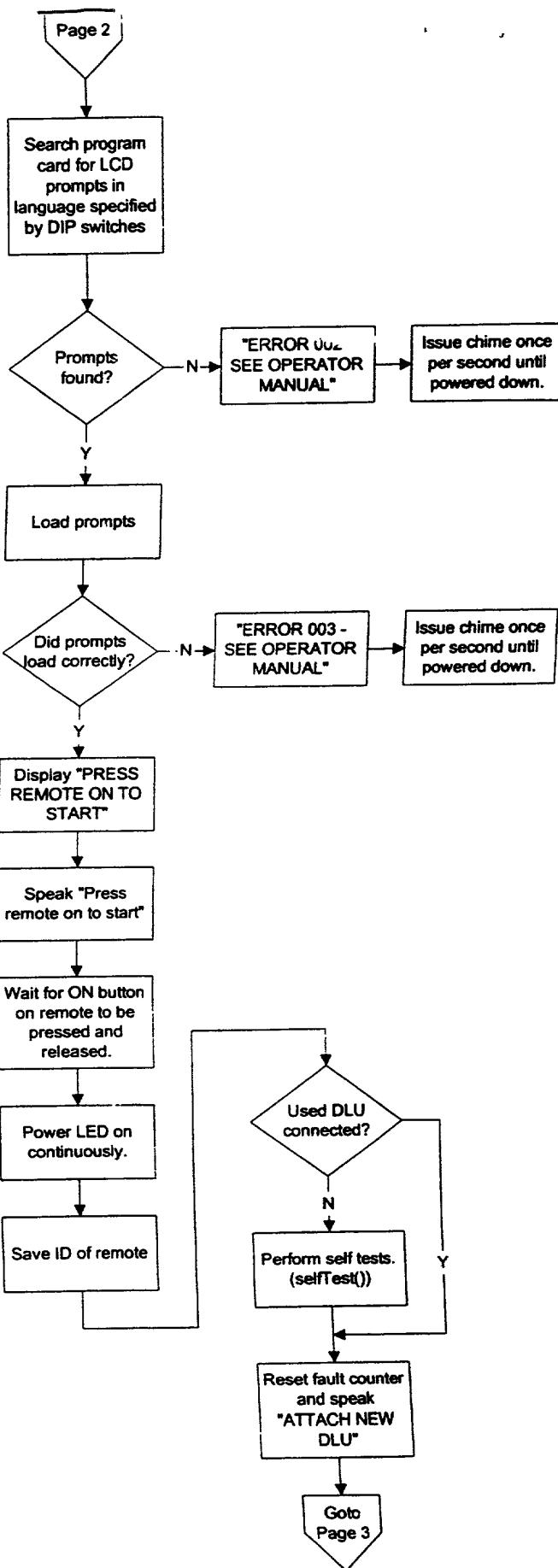


FIG. 17a

FIG. 17b



Main processing loop

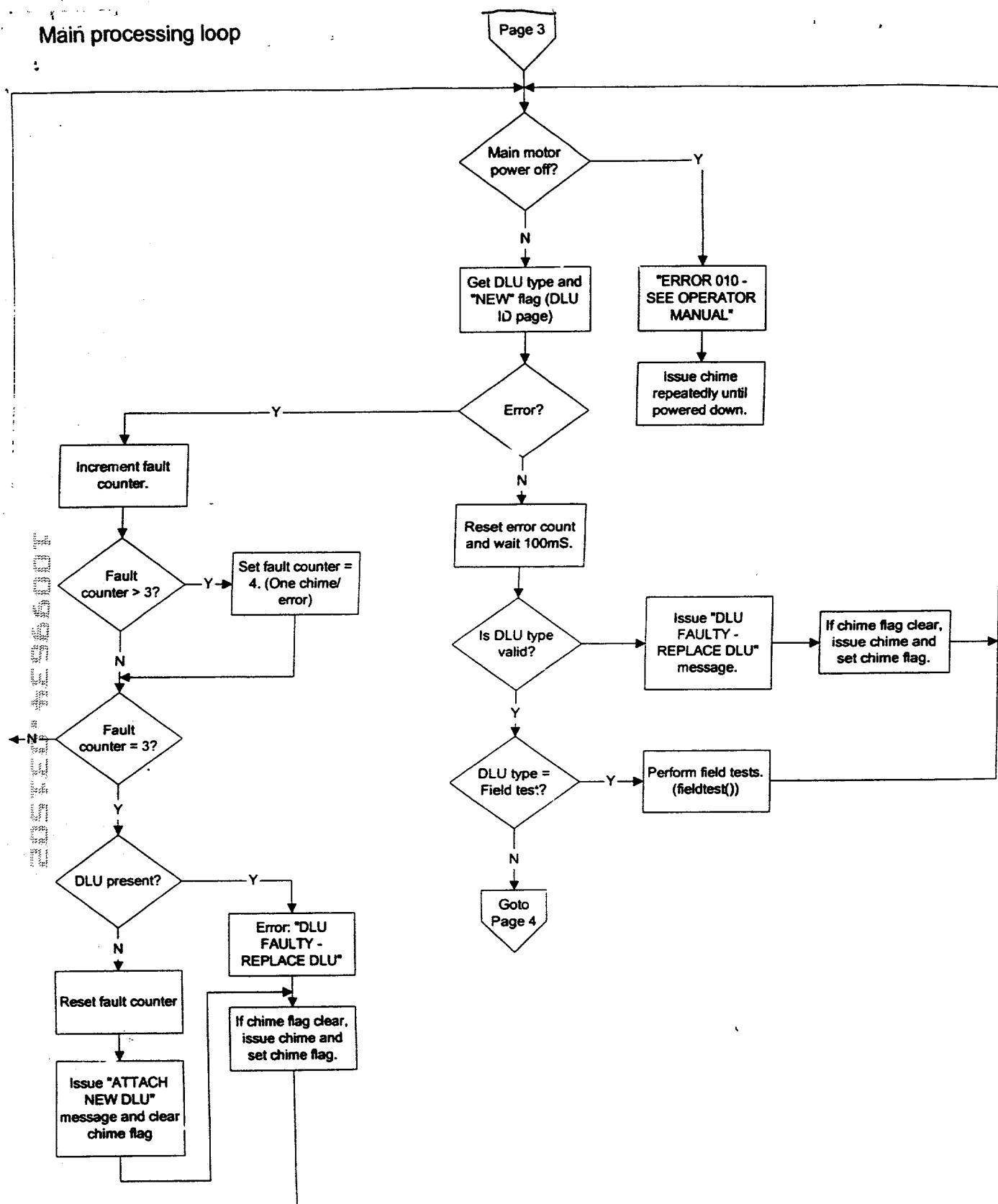


FIG. 19c

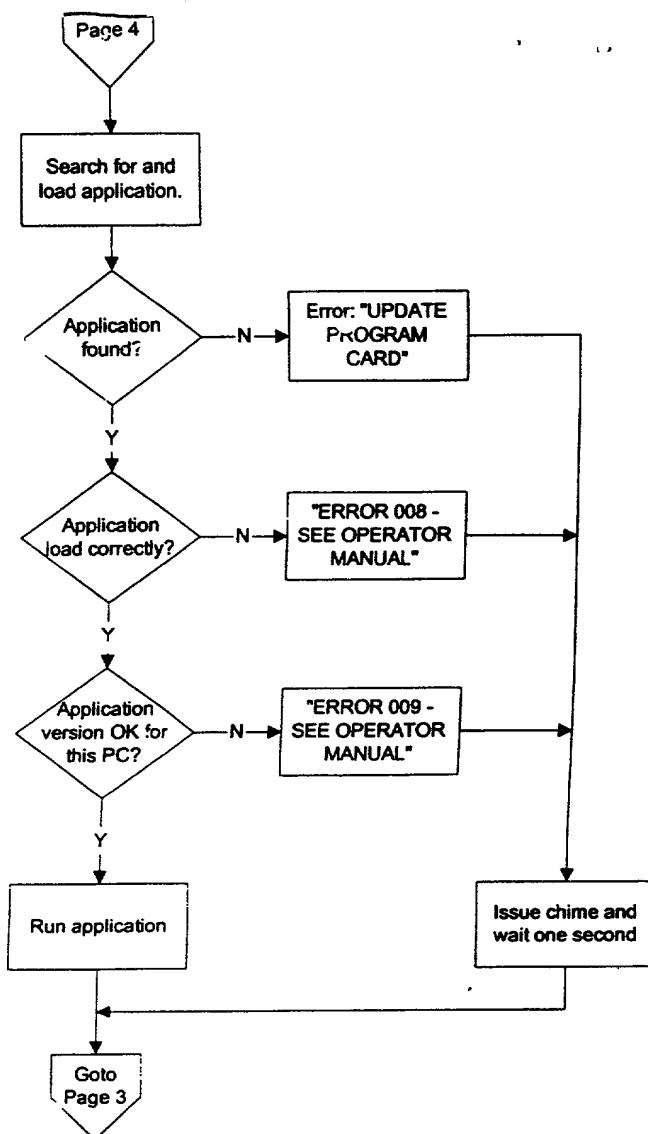


FIG. 17d

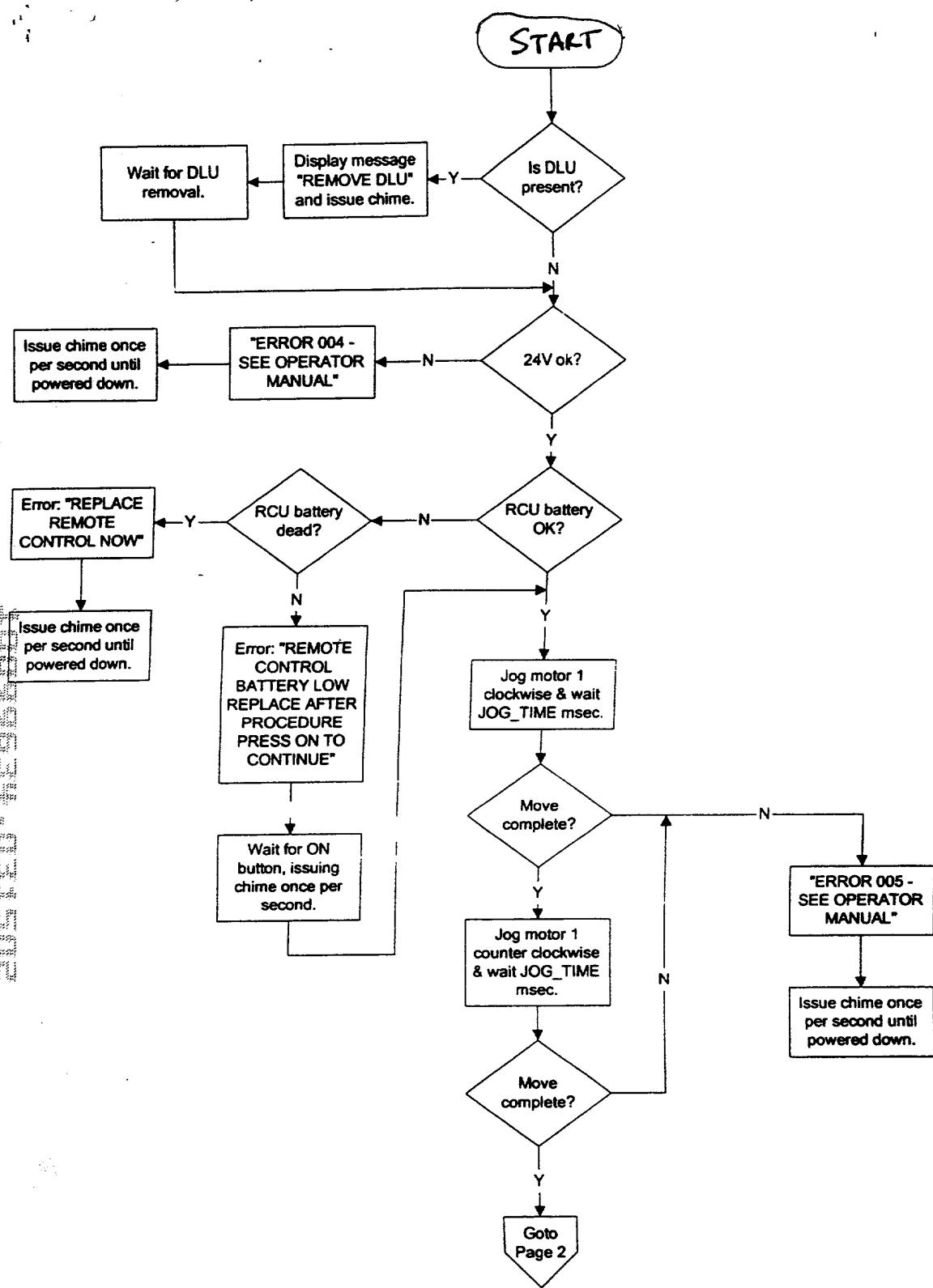


FIG. 18a

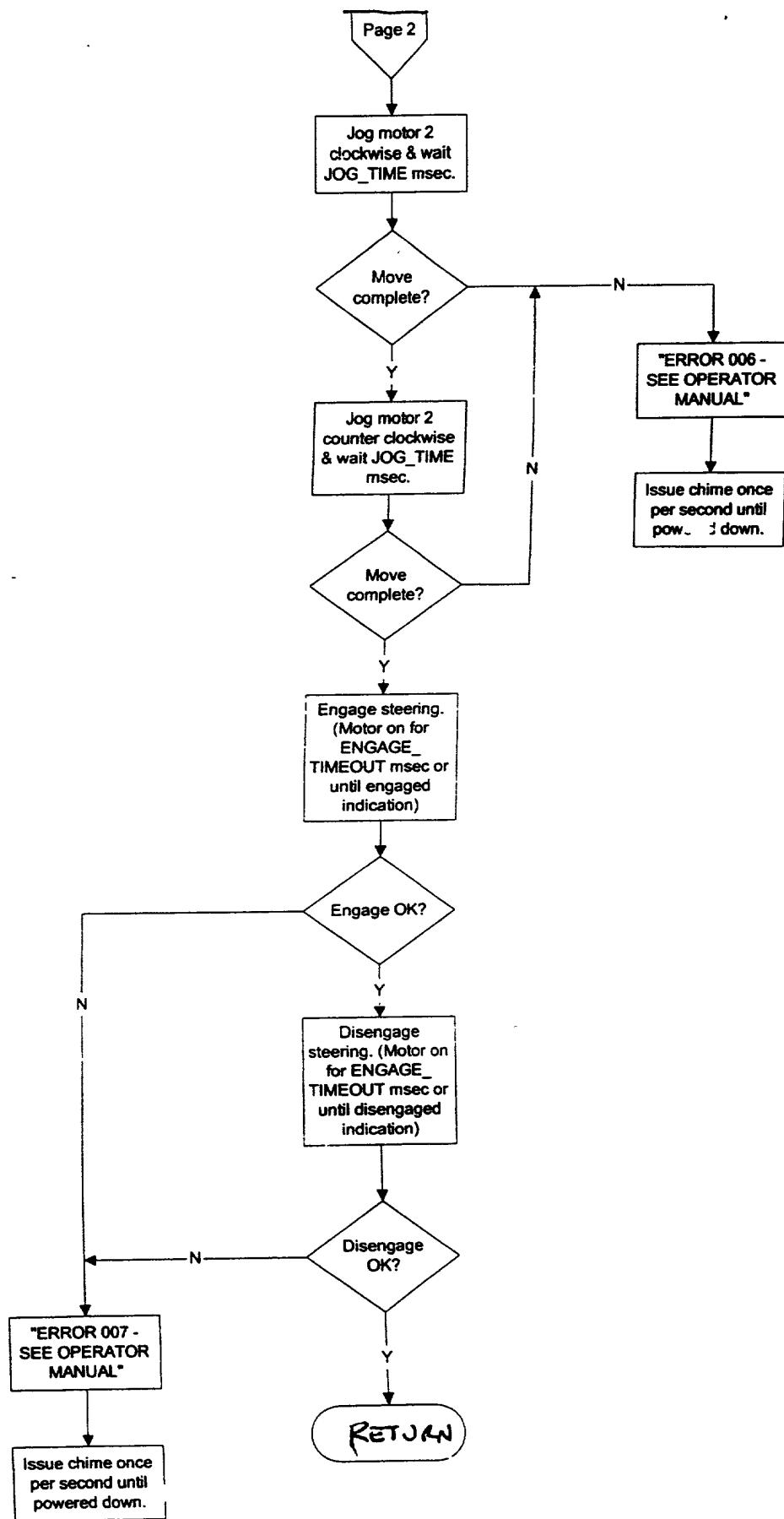
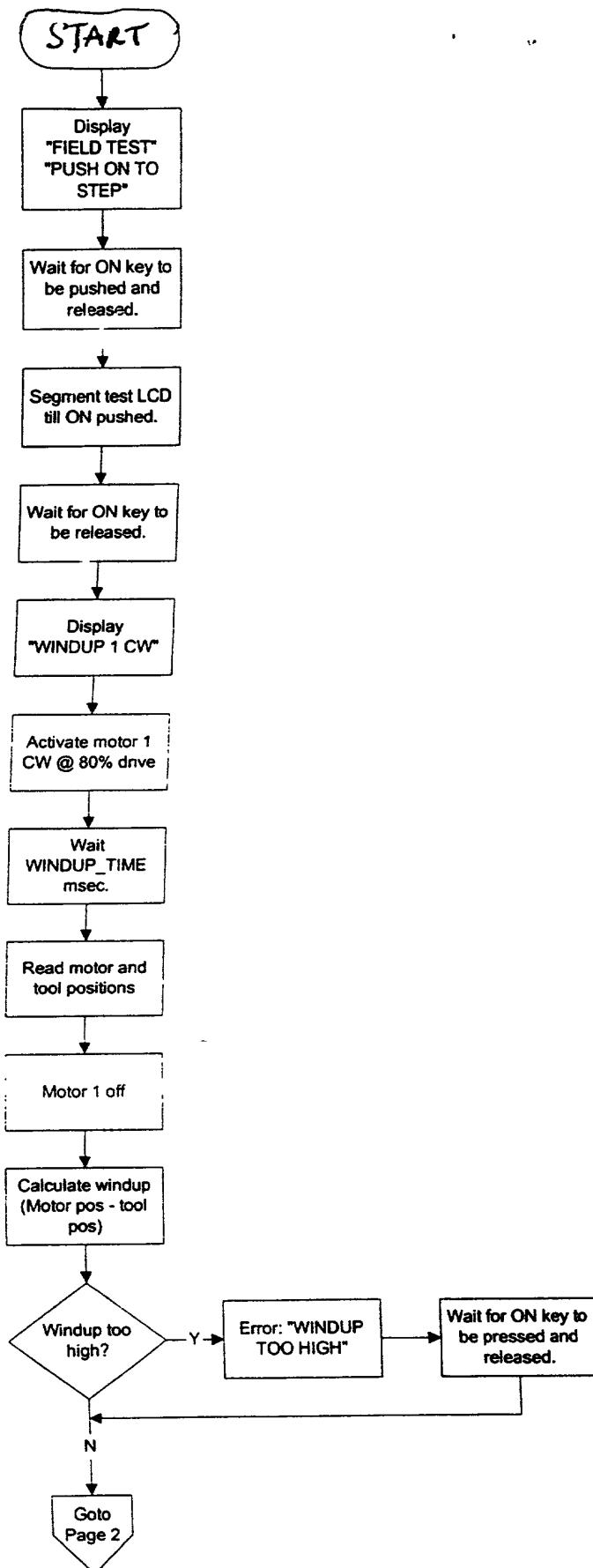


FIG. 18b

100 200 300 400 500 600 700 800 900

FIG. 19a



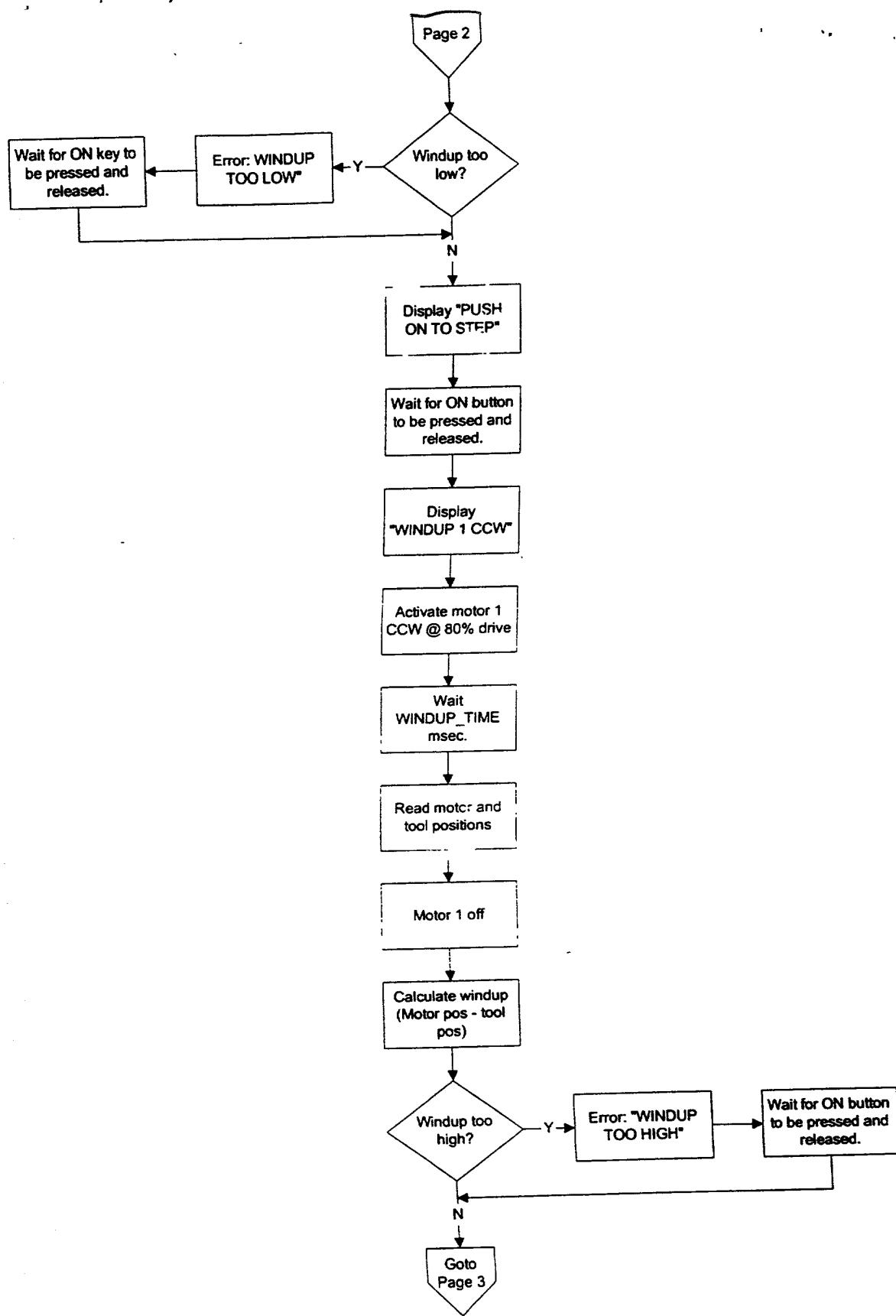


FIG. 19b

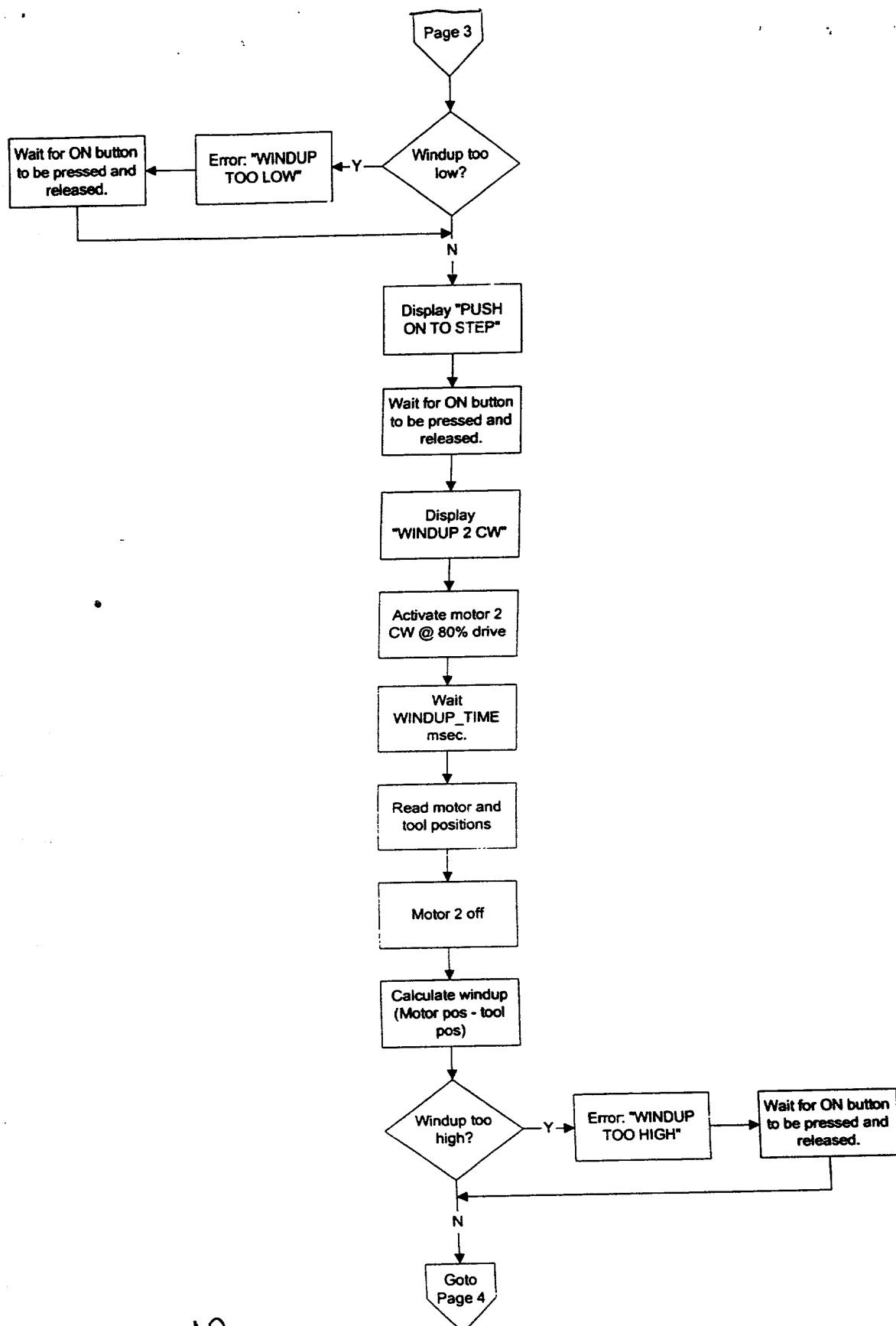


FIG. 19c

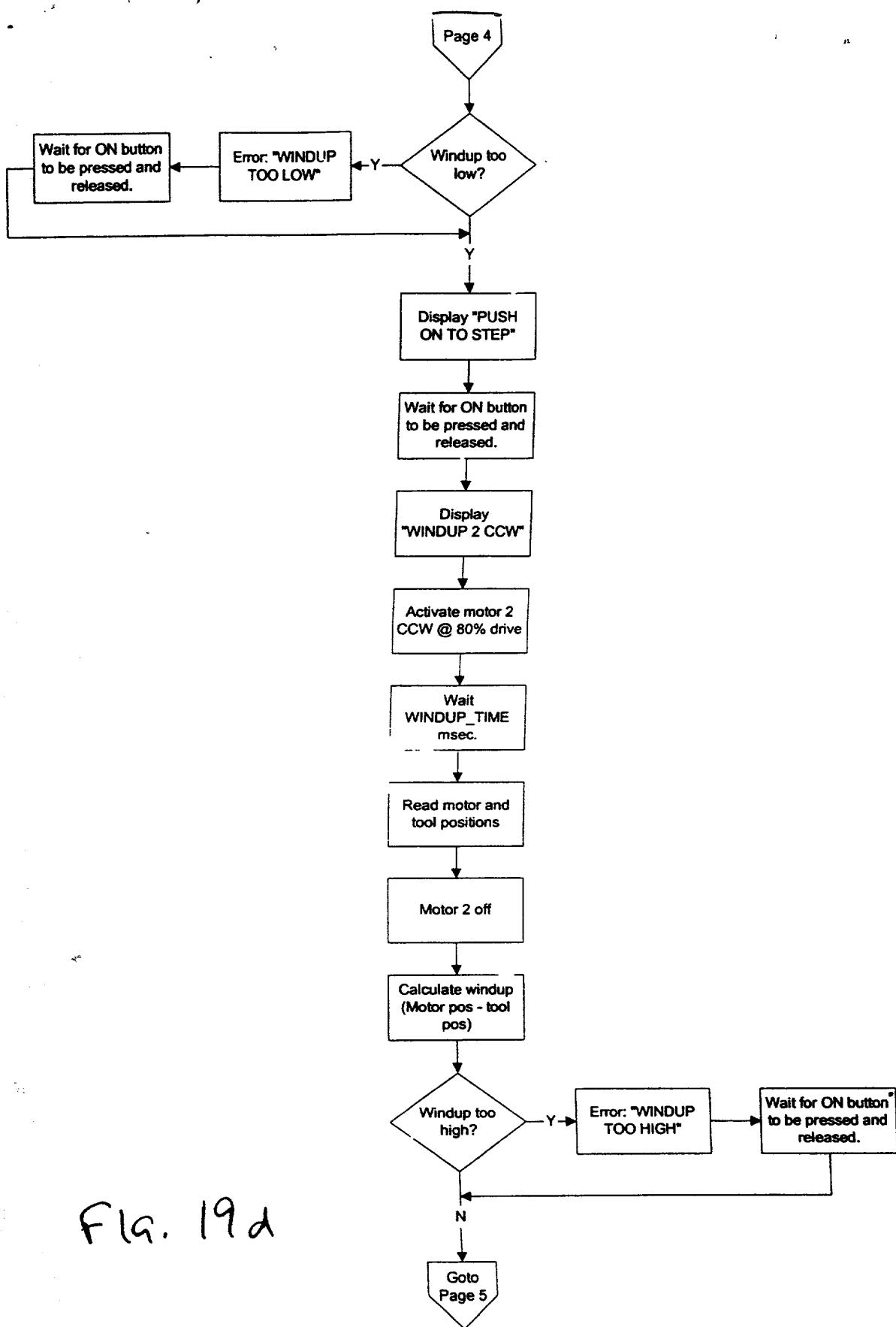


Fig. 19d

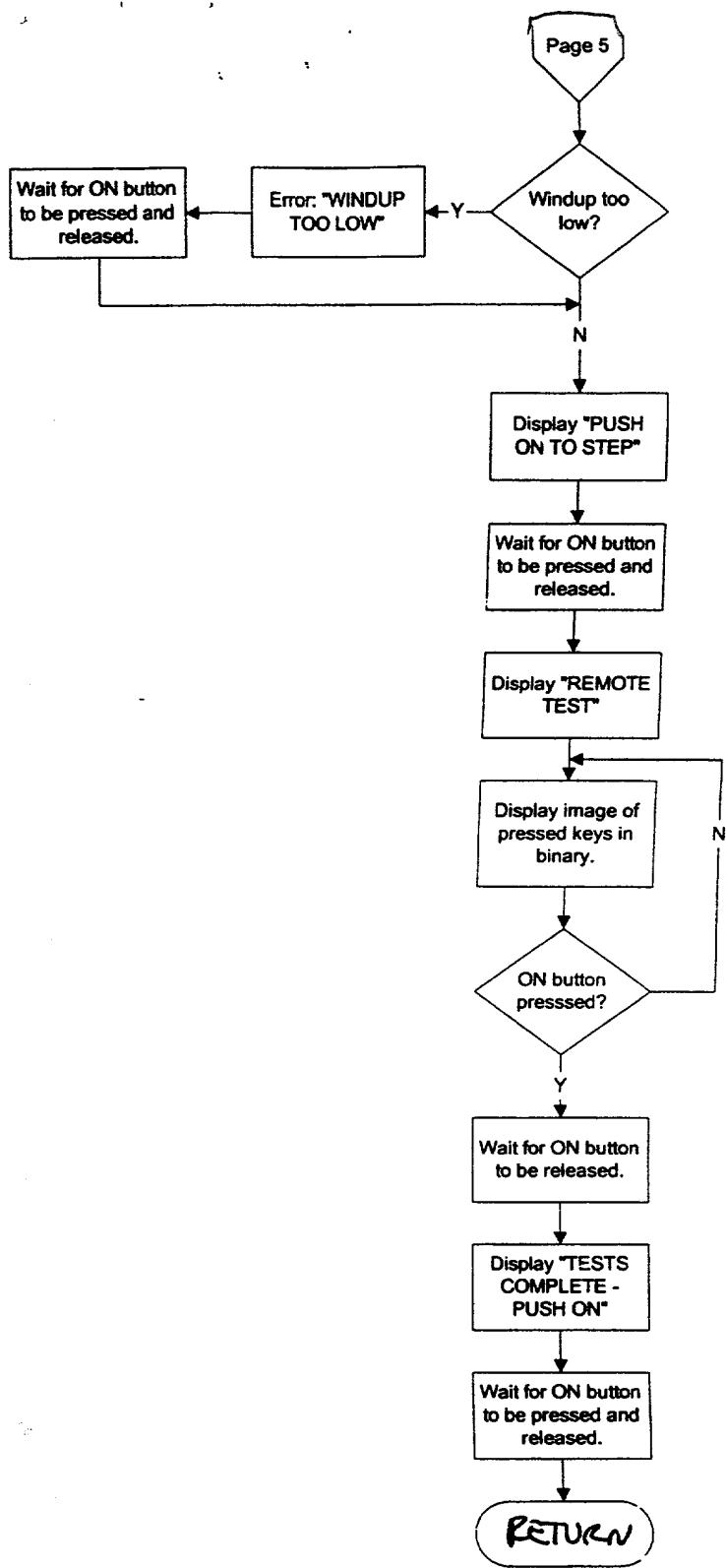


FIG. 19e

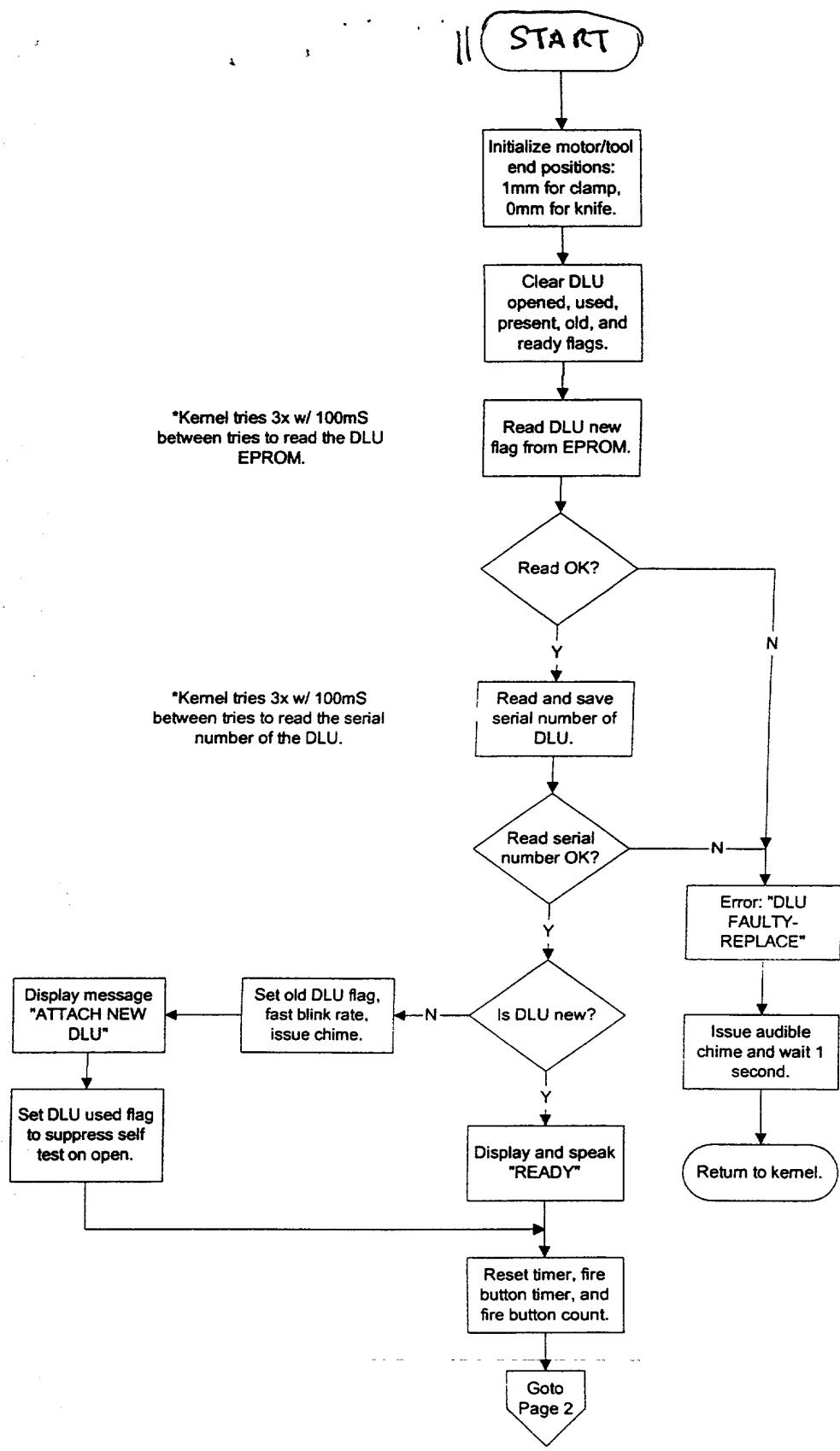


FIG. 20a

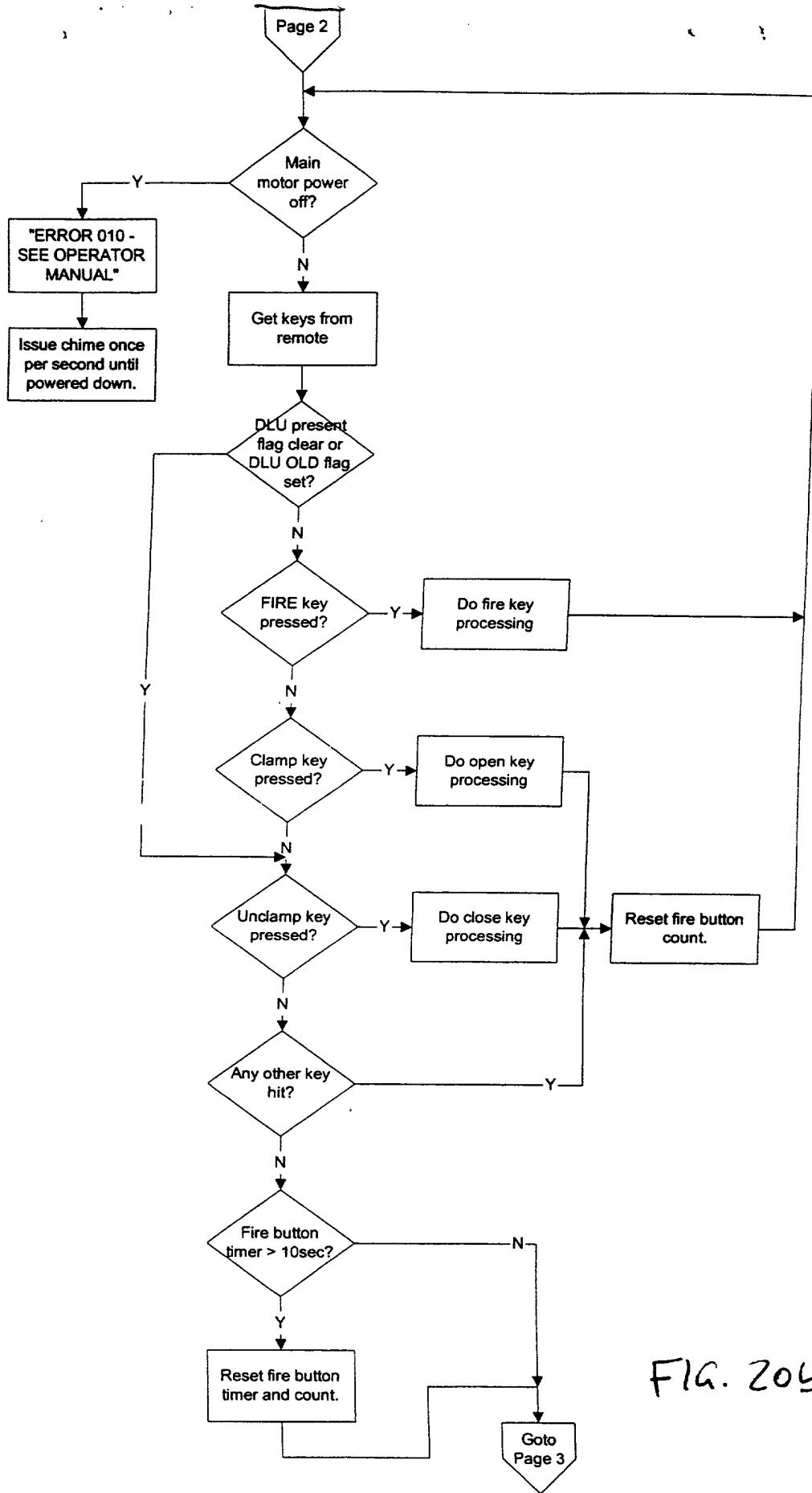
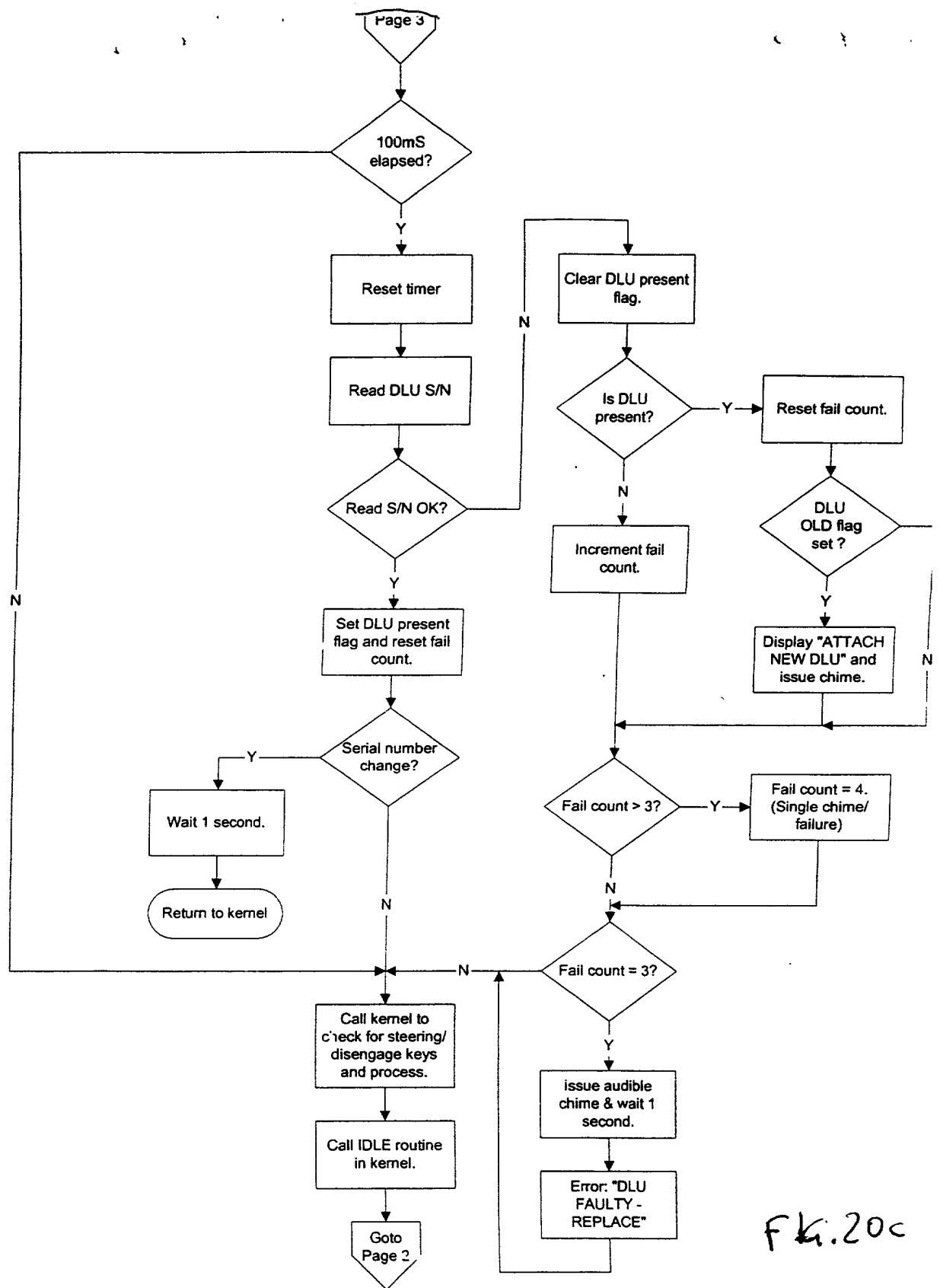


FIG. 205



fk.20c

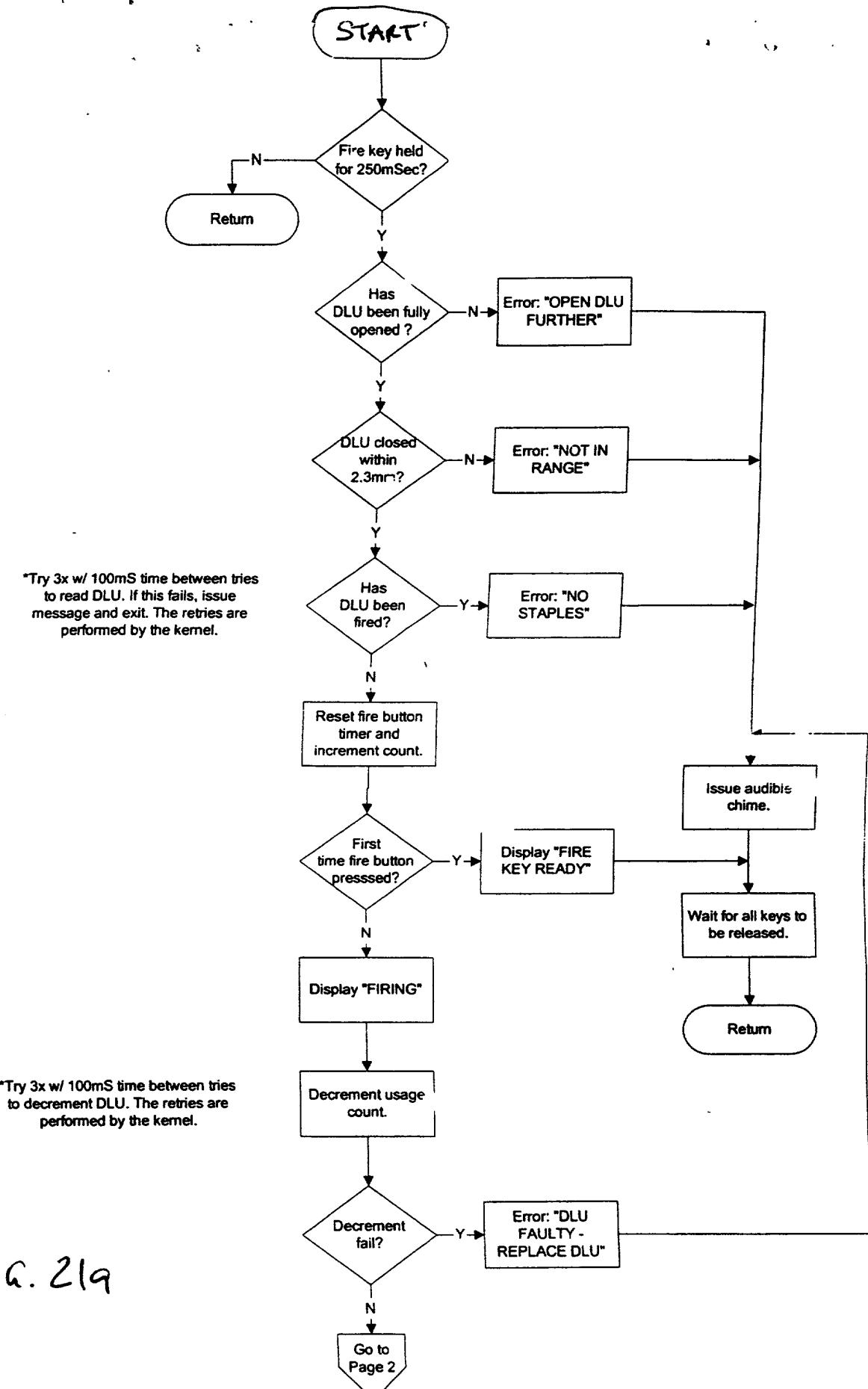


FIG. 219

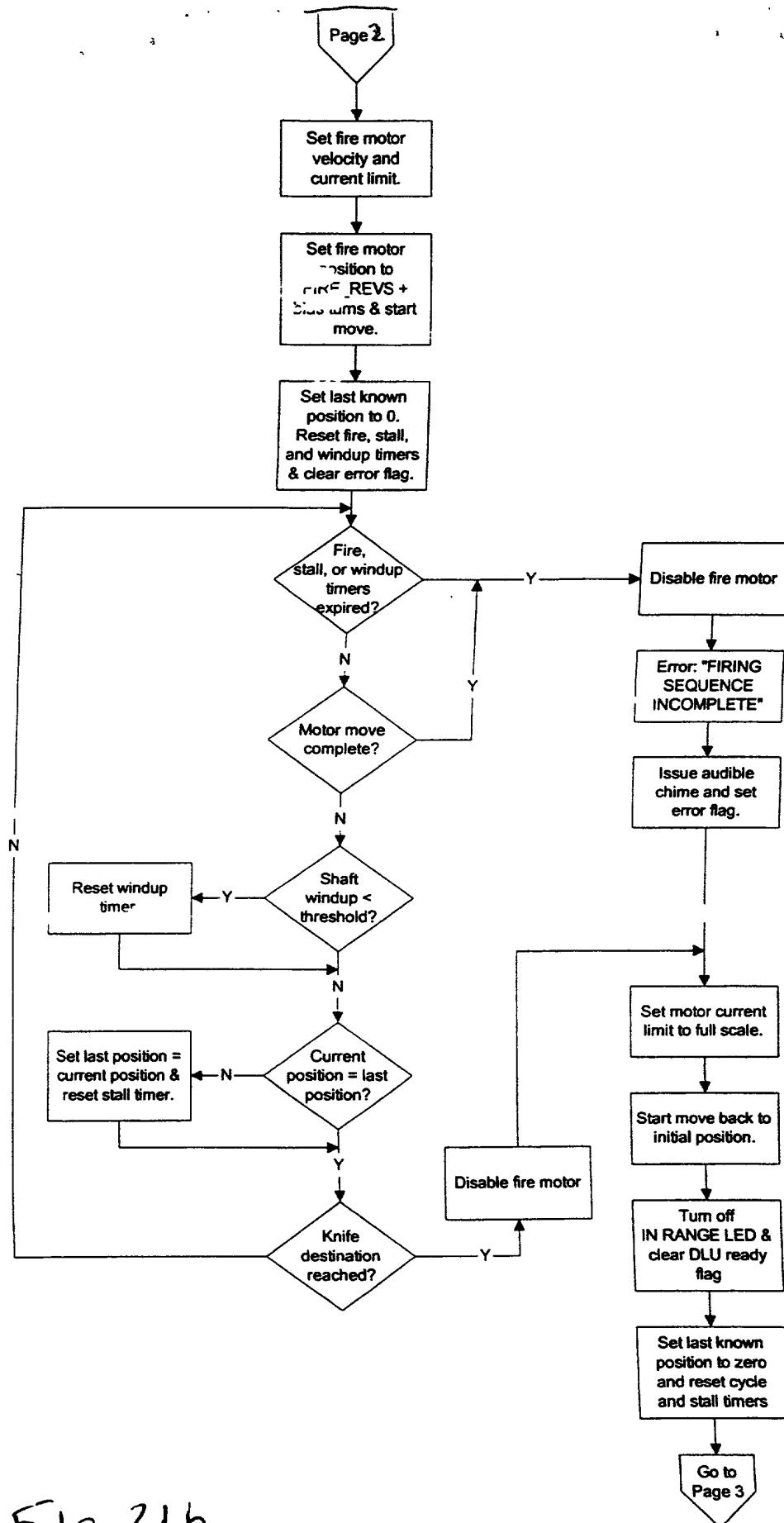


Fig. 21b

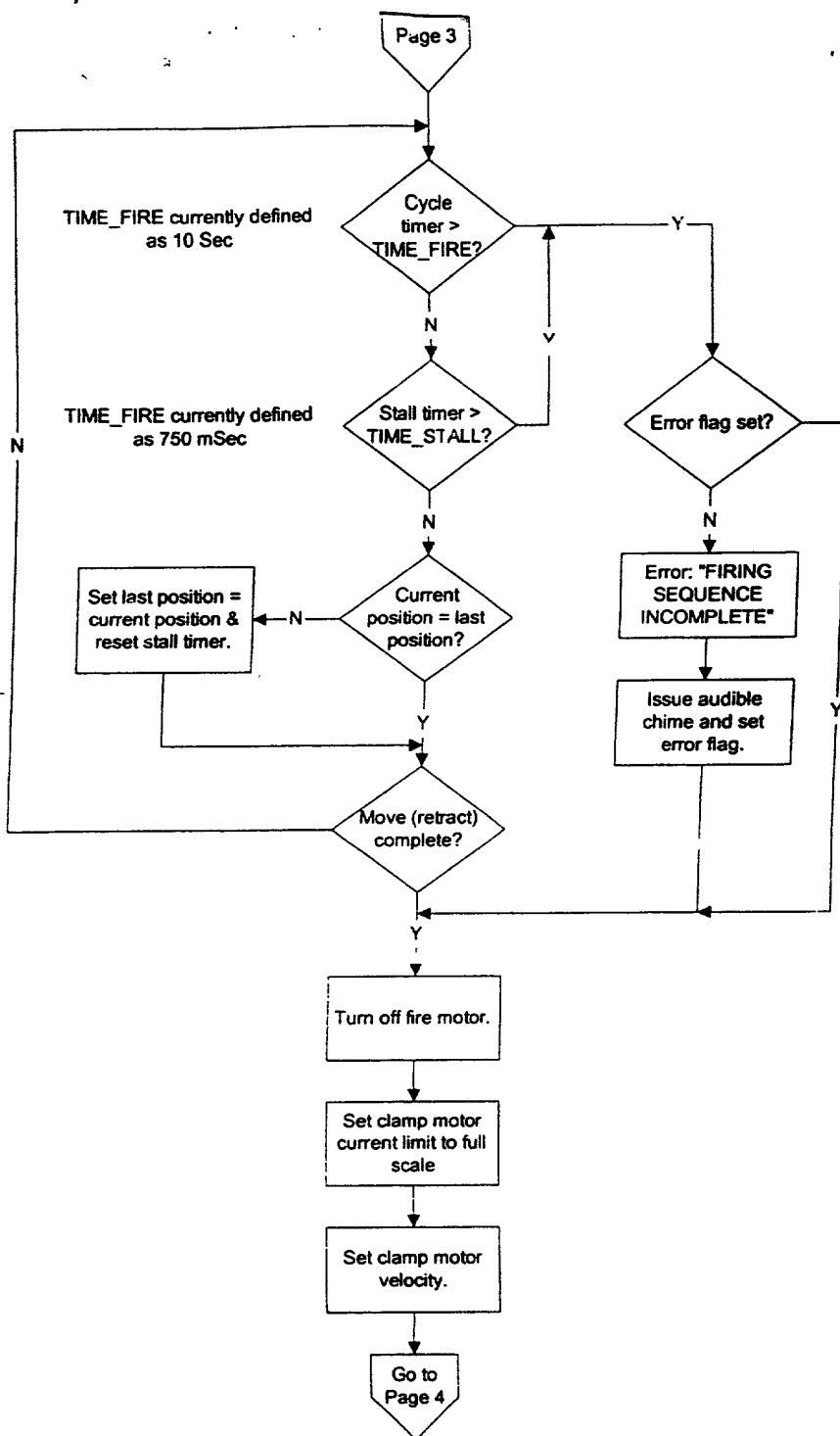


FIG. Z1c

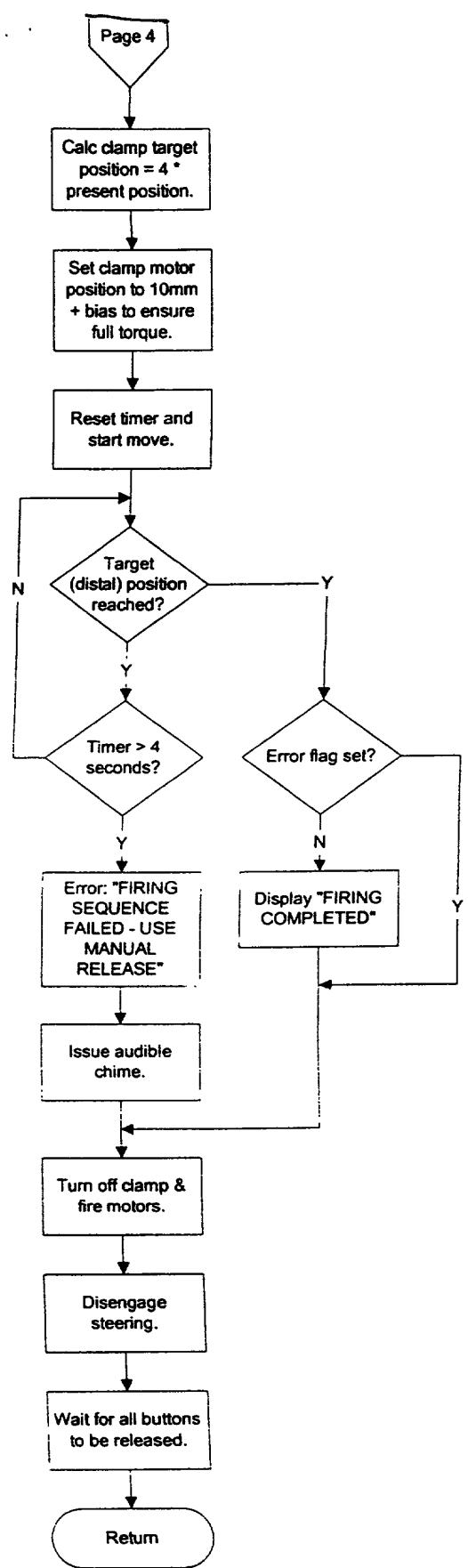


FIG. 21d

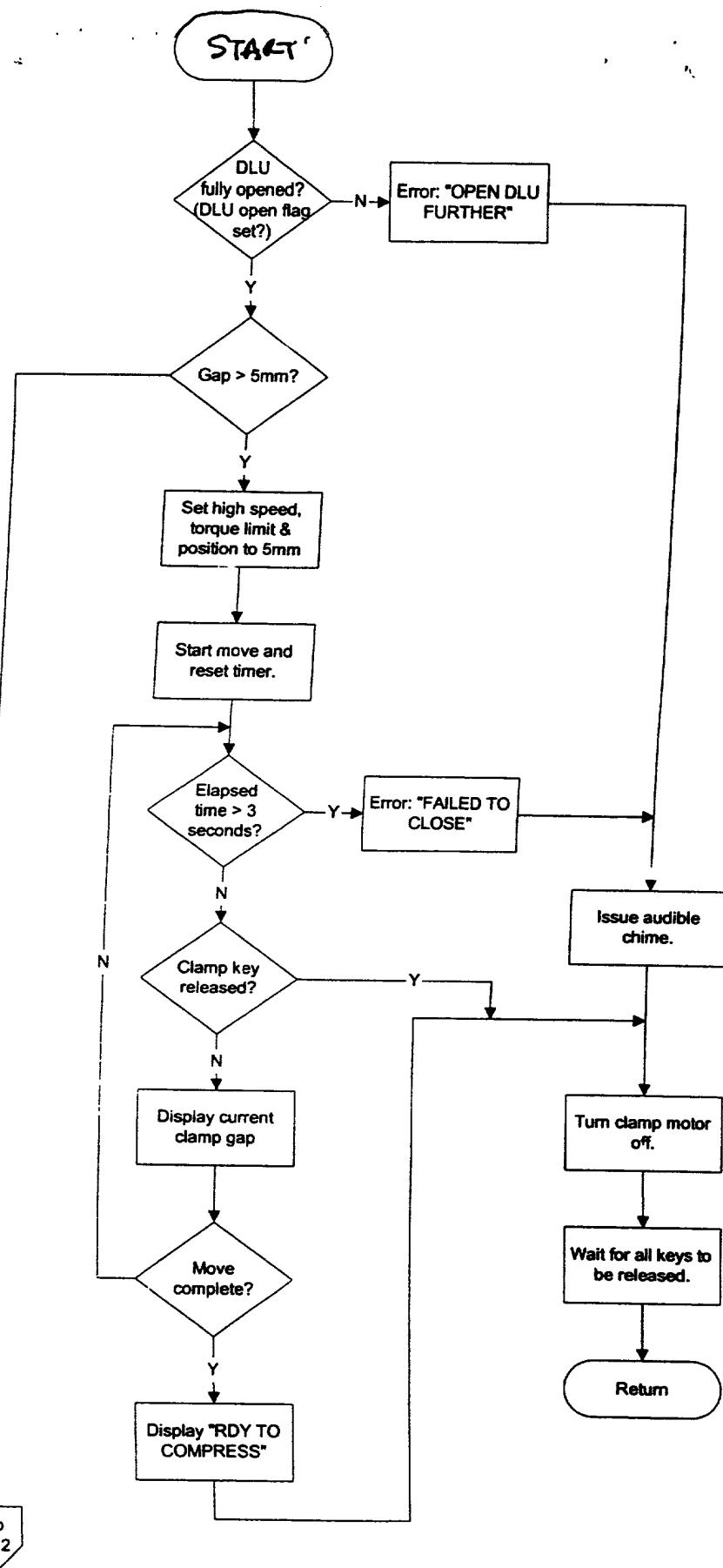


FIG. 22a

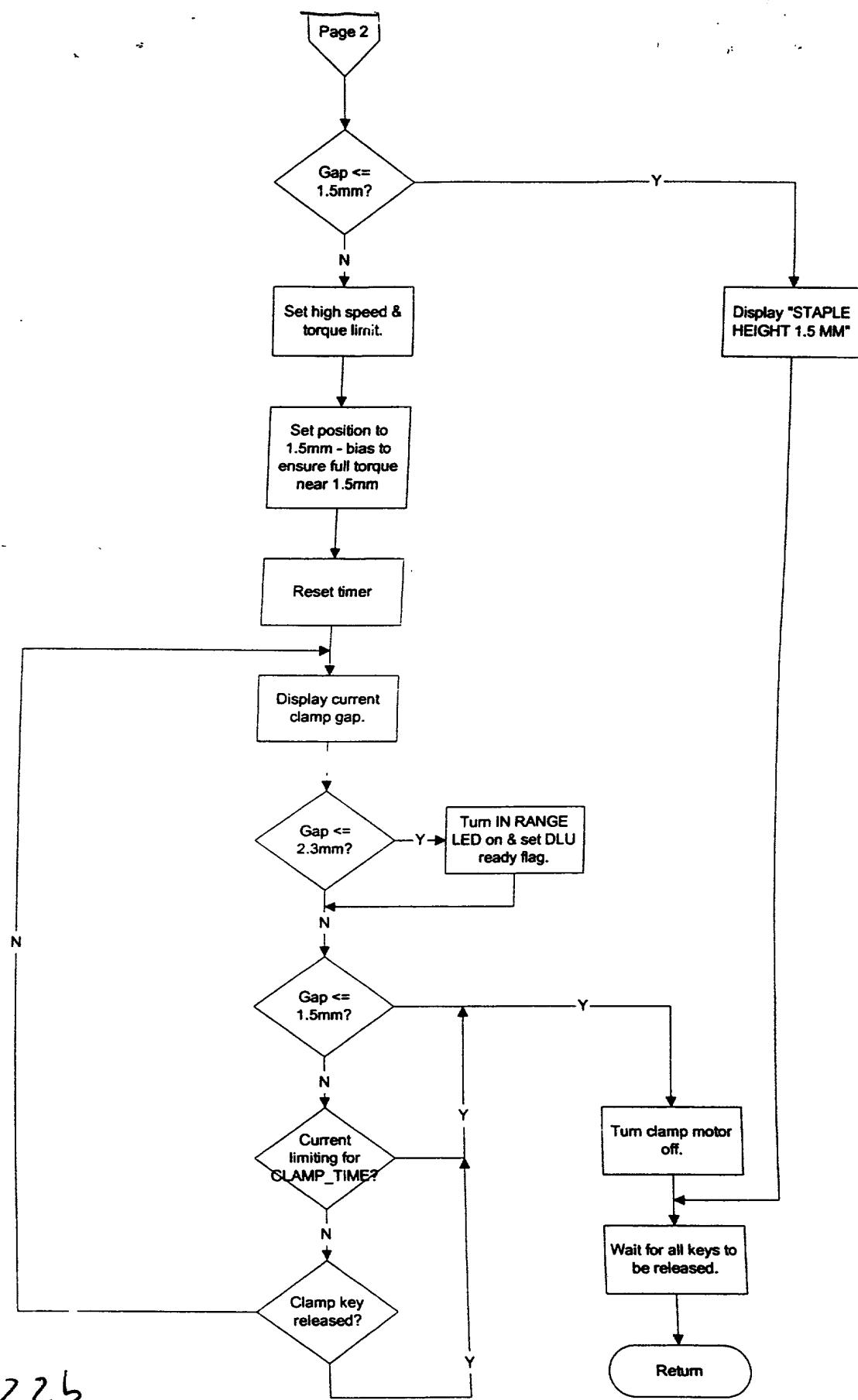


FIG. 225

*The kernel tries 3x w/ 100mS time
between tries to write DLU.

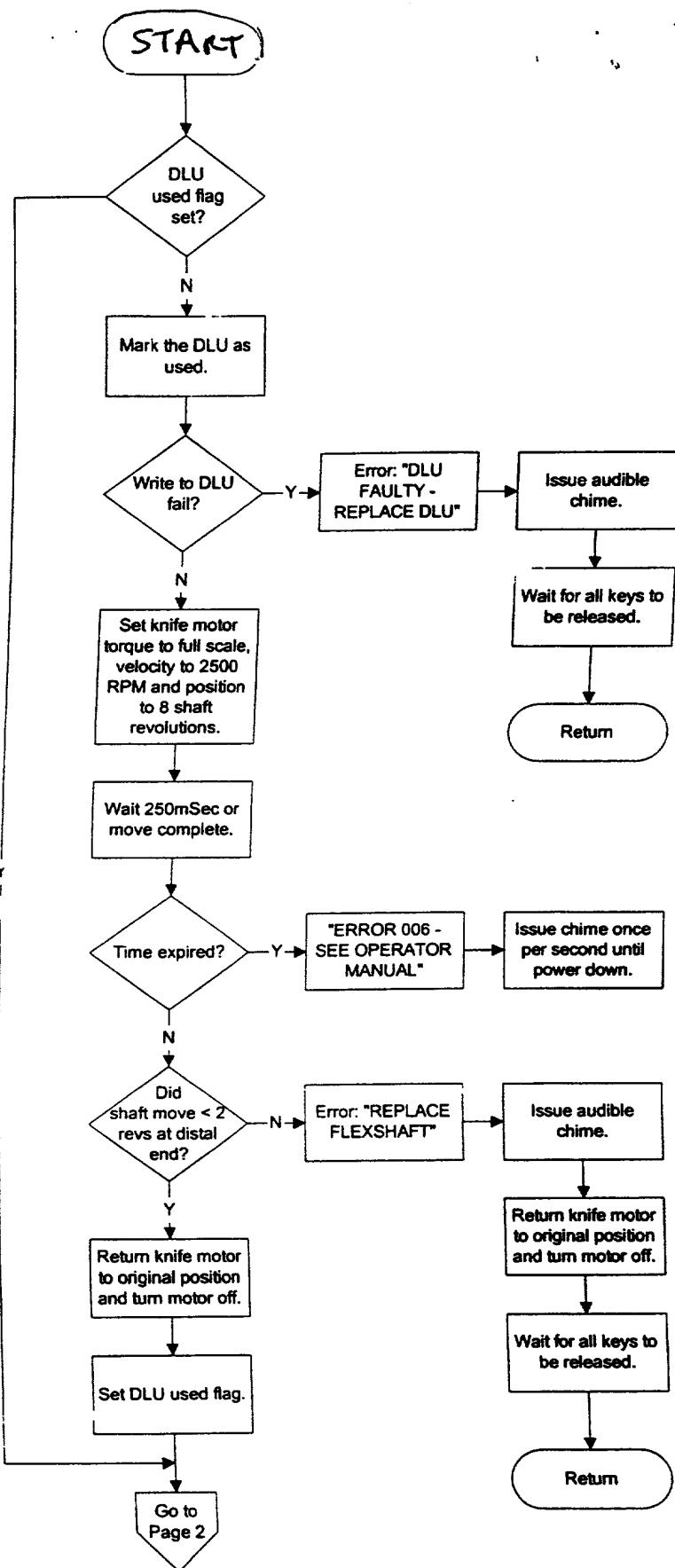


FIG. 23a

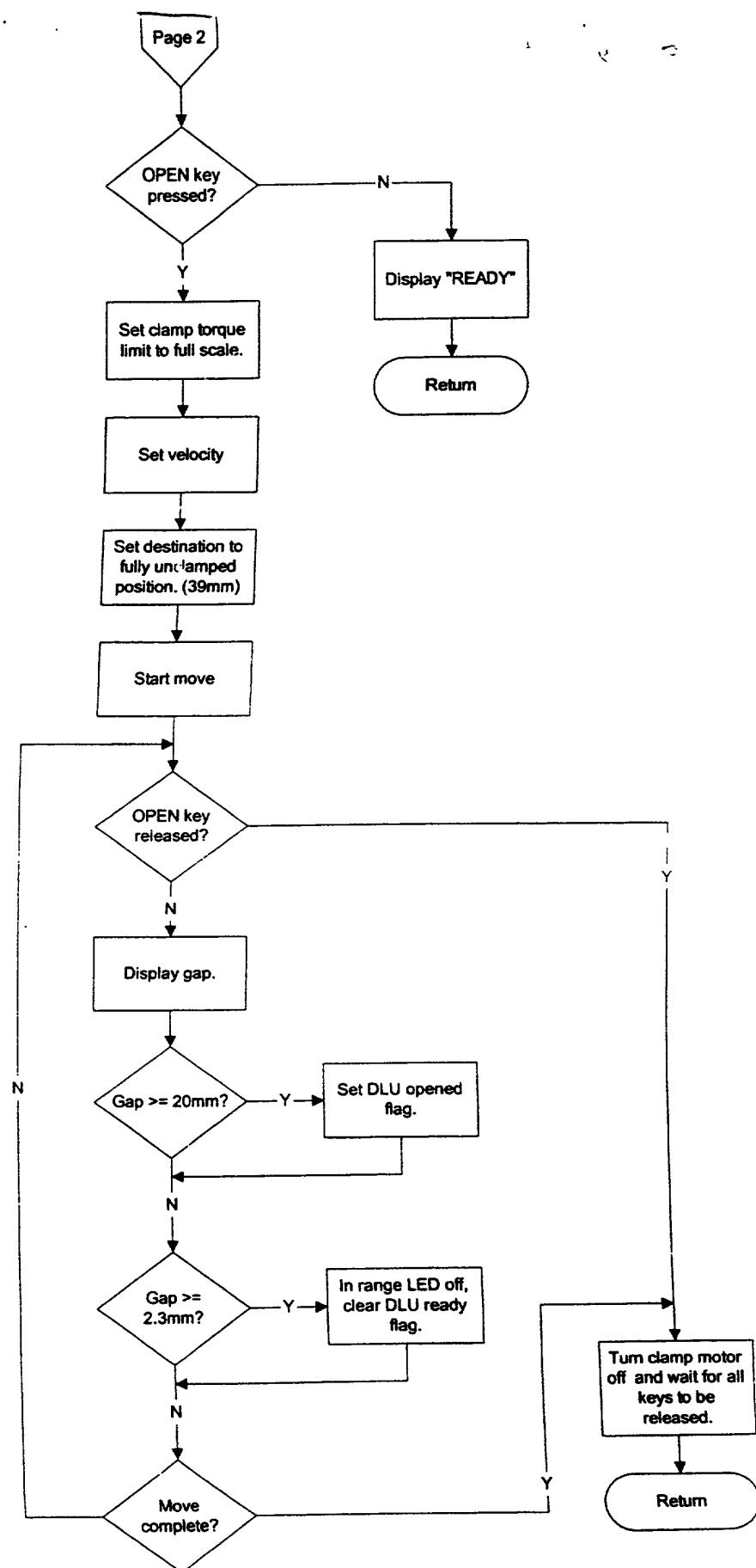


FIG. 23b

Figure 24Q

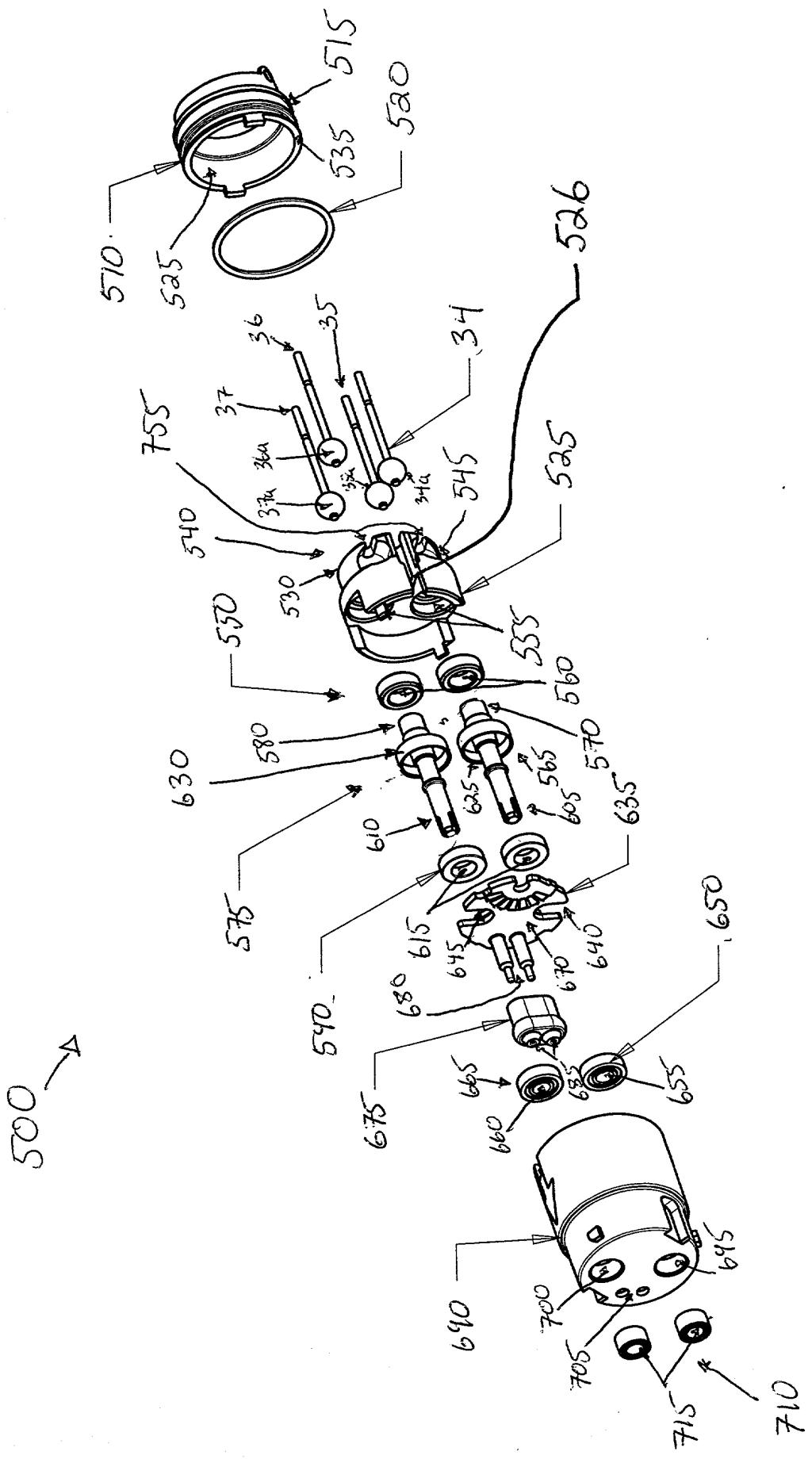


Figure 24b

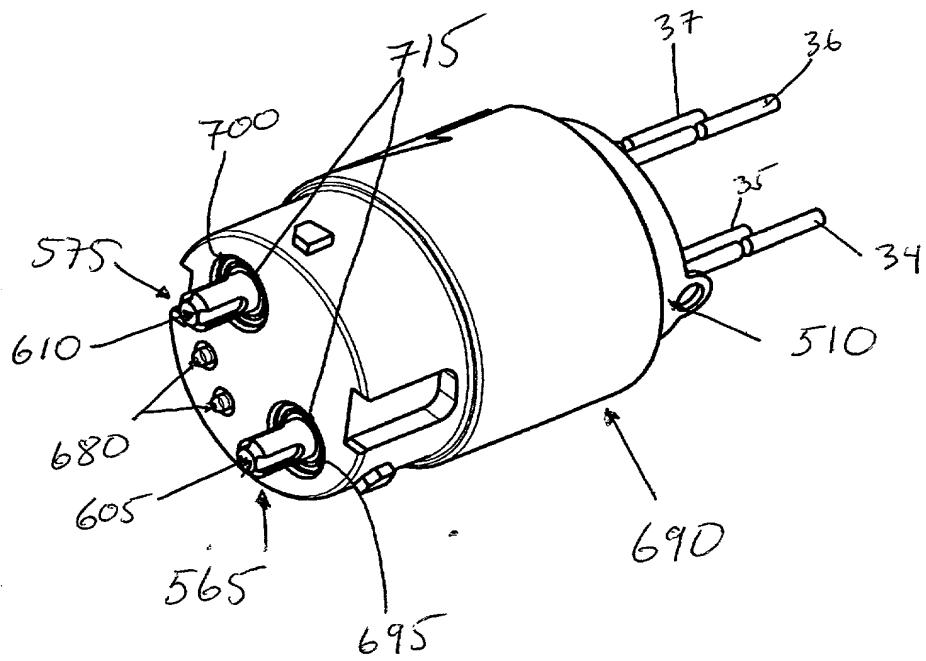


Figure 25

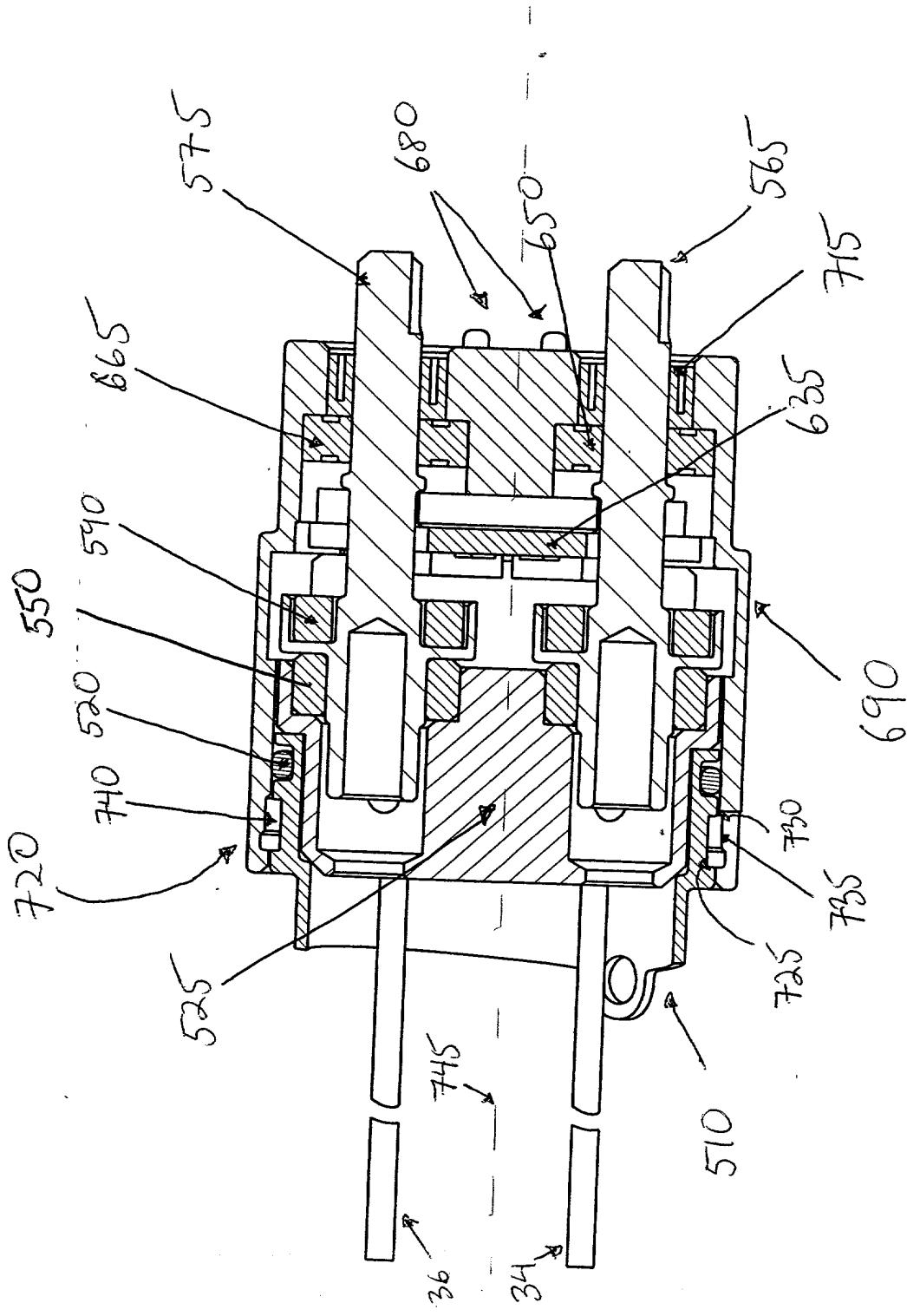


Figure 26a

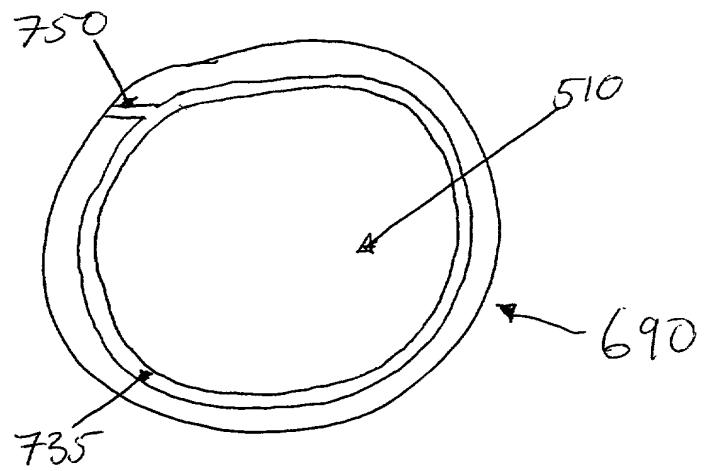


Figure 26b

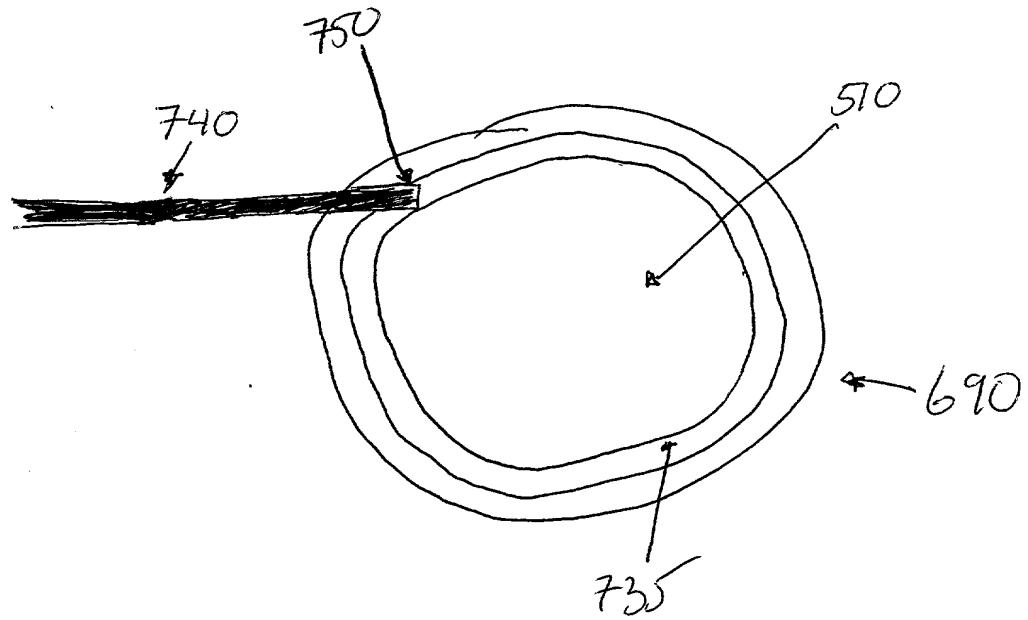


Figure 26c

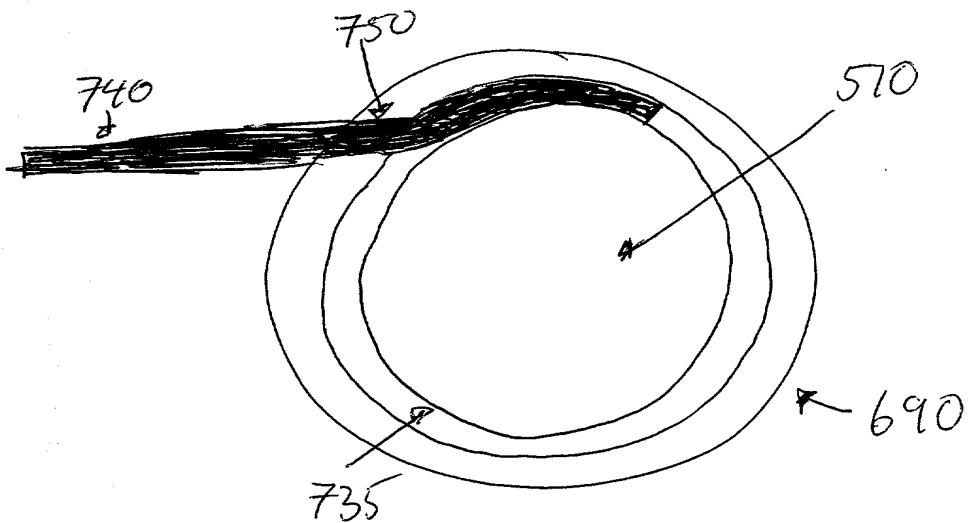


Figure 26d

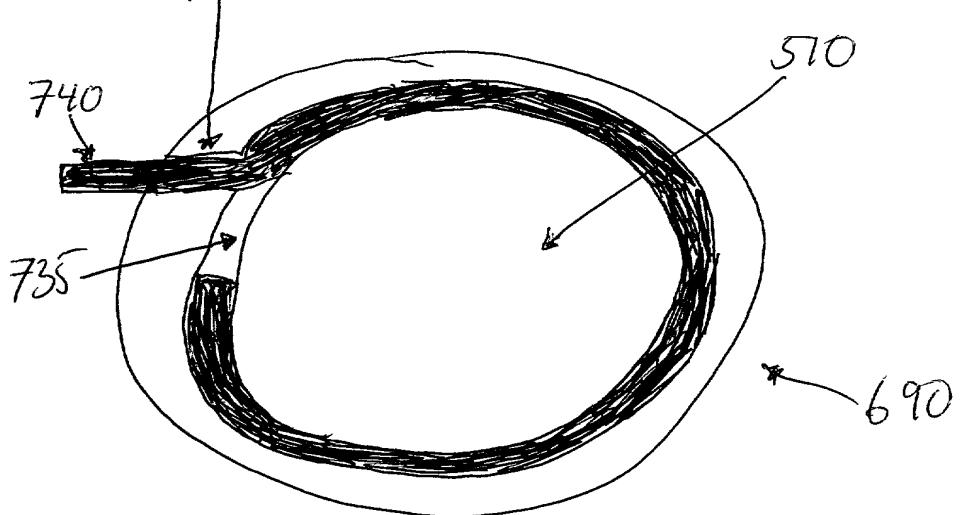


Figure 24

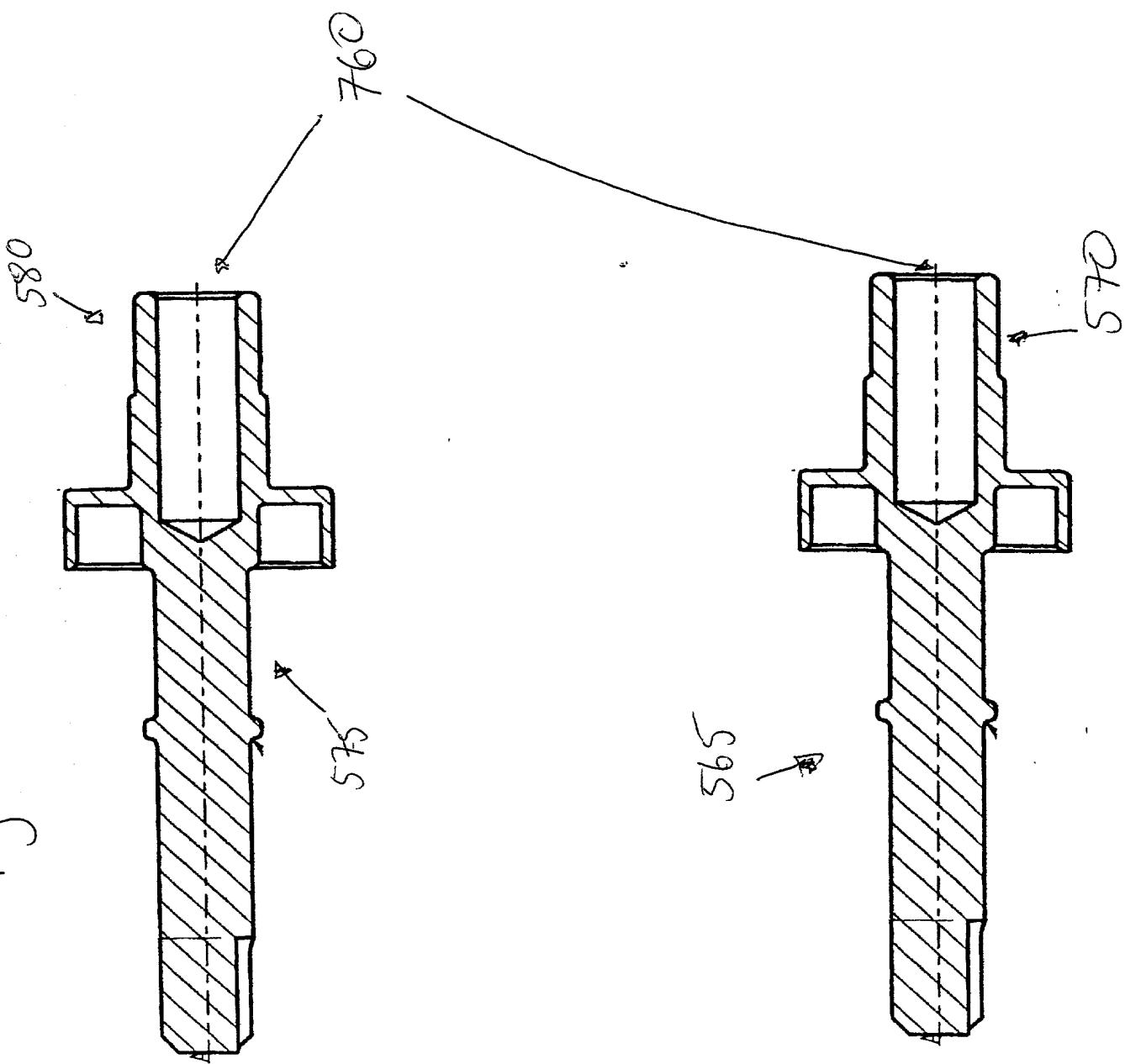


Figure 28a

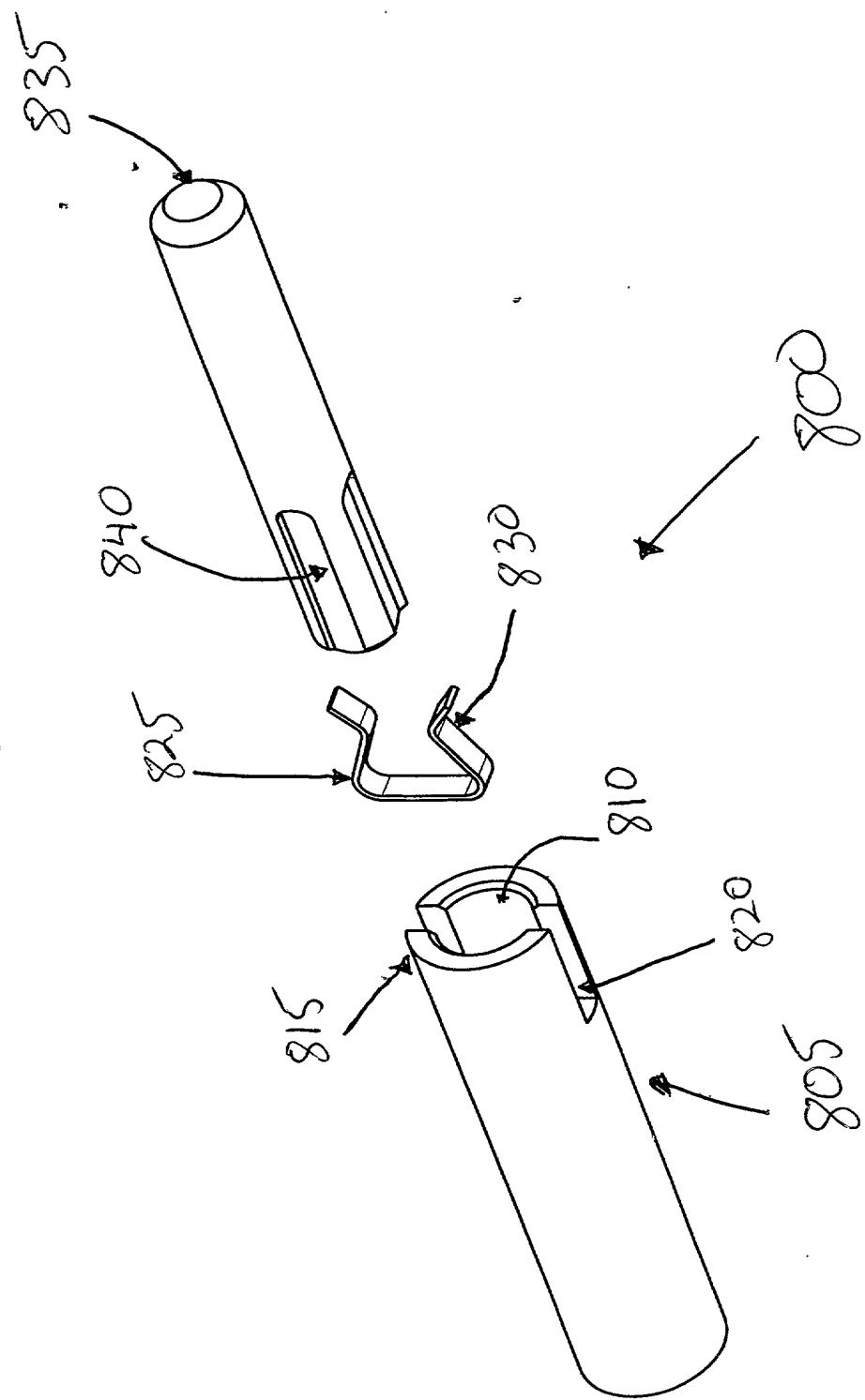


Figure 289D

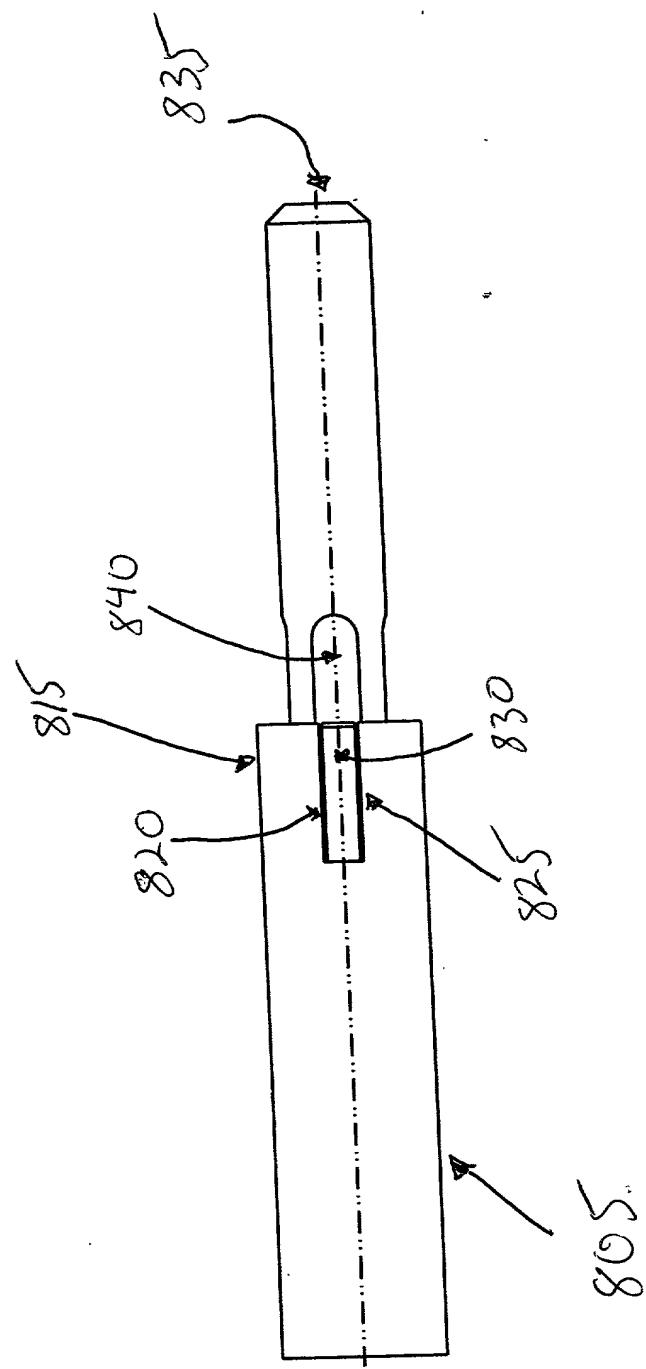


Figure 28C

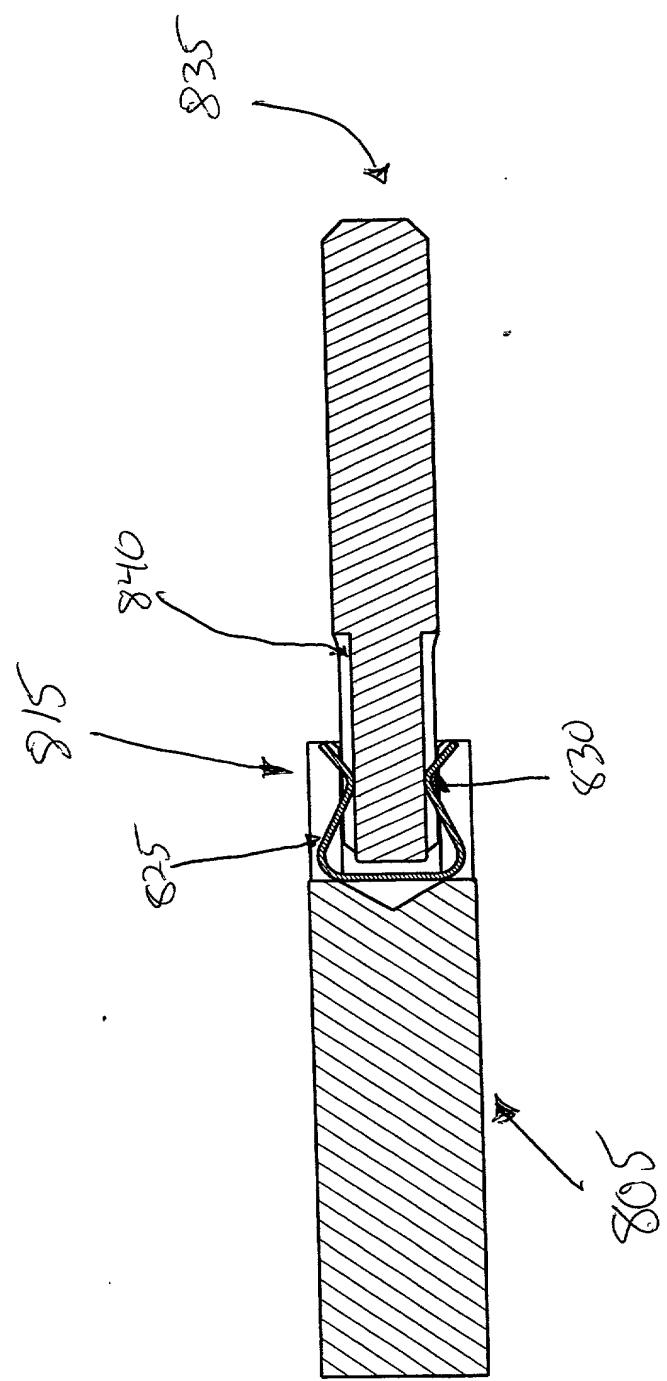


Figure 2 of 966000

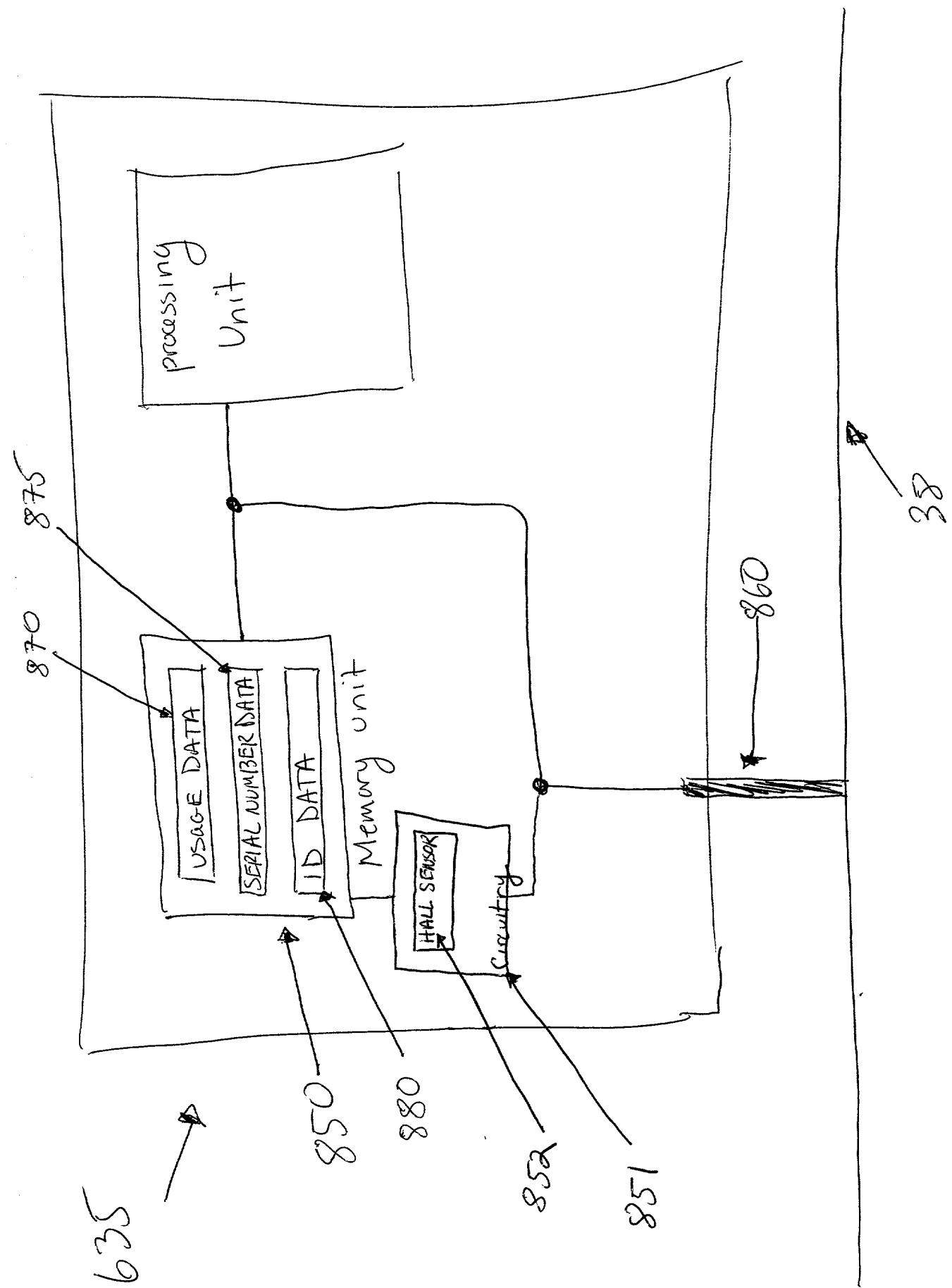


Figure 30a *and* **30b** *Diagram illustrating the effect of the variation of the parameter α on the solution of the system of equations (1)–(3).*

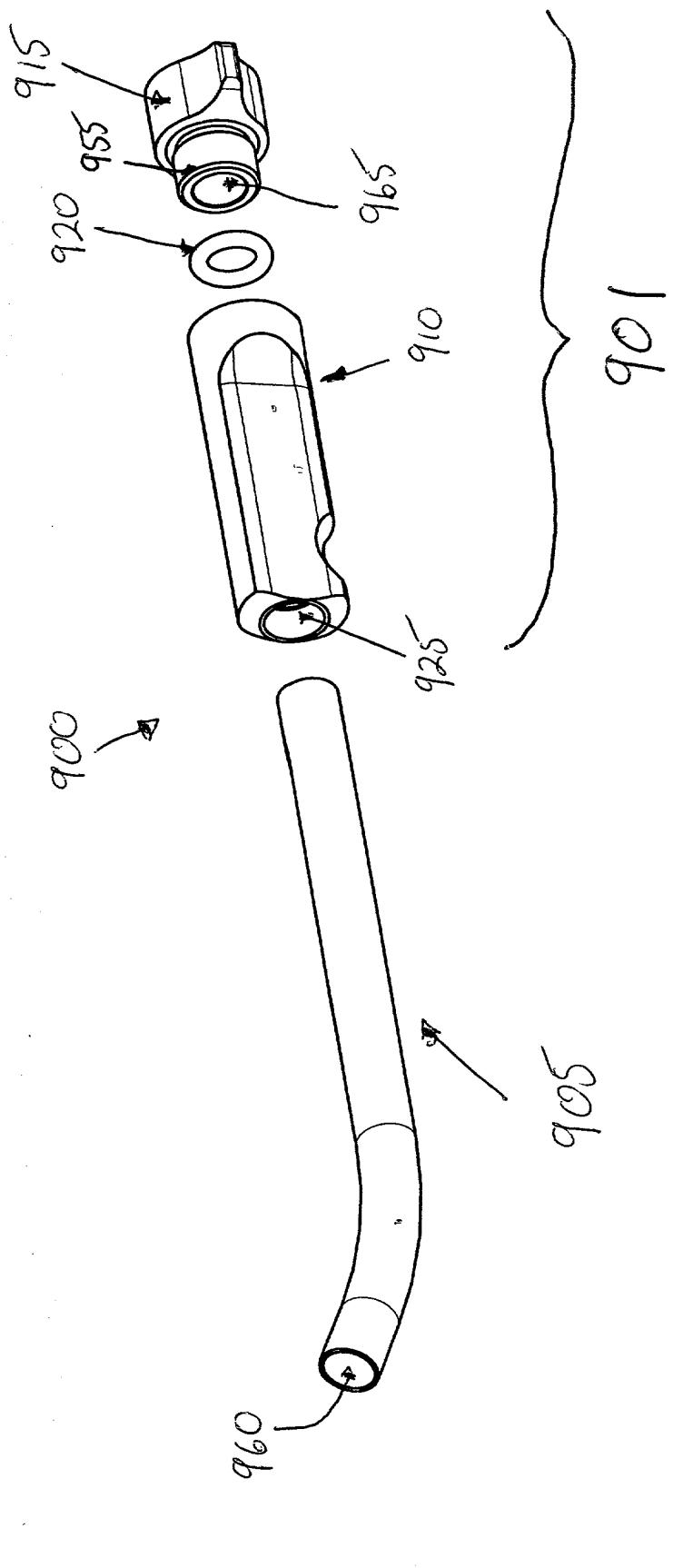


Figure 3D_b

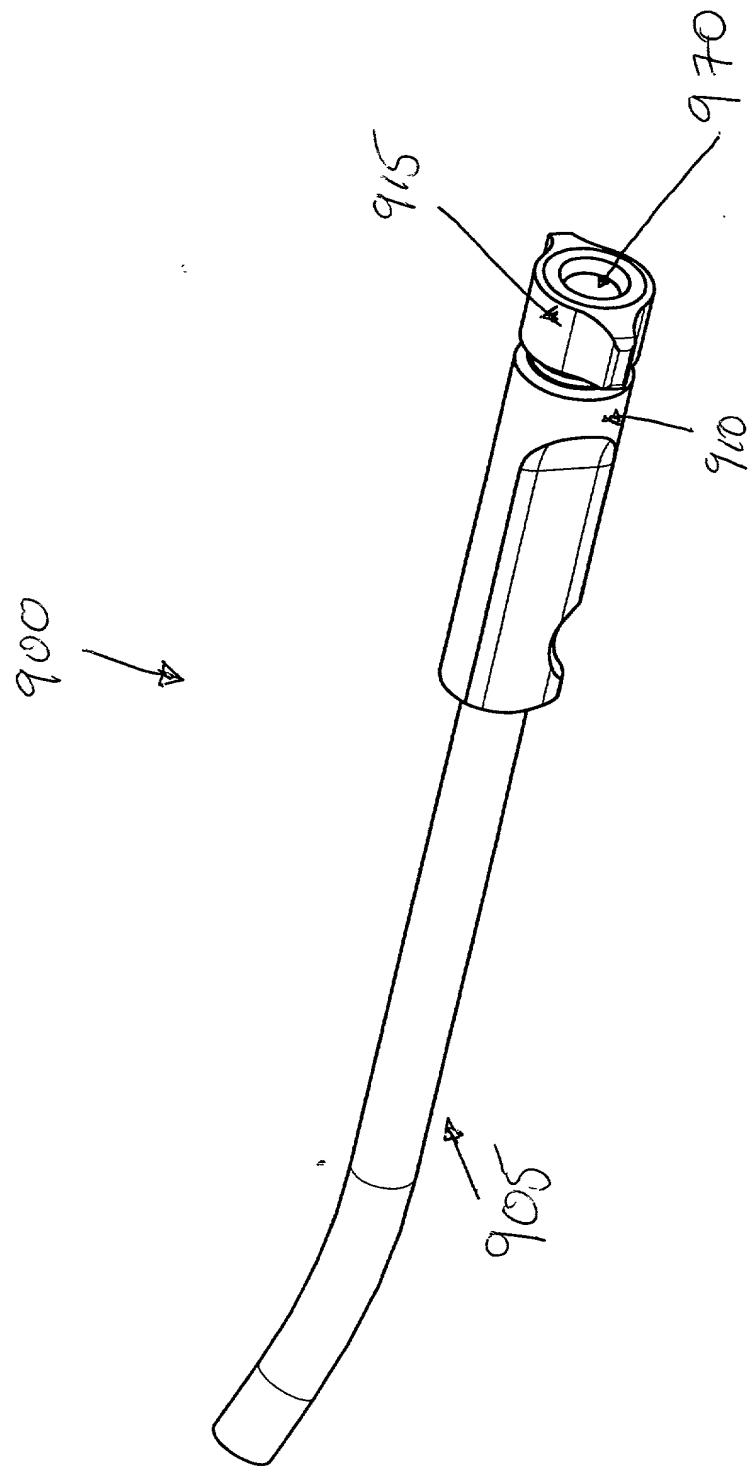


Figure 30C-355G1W

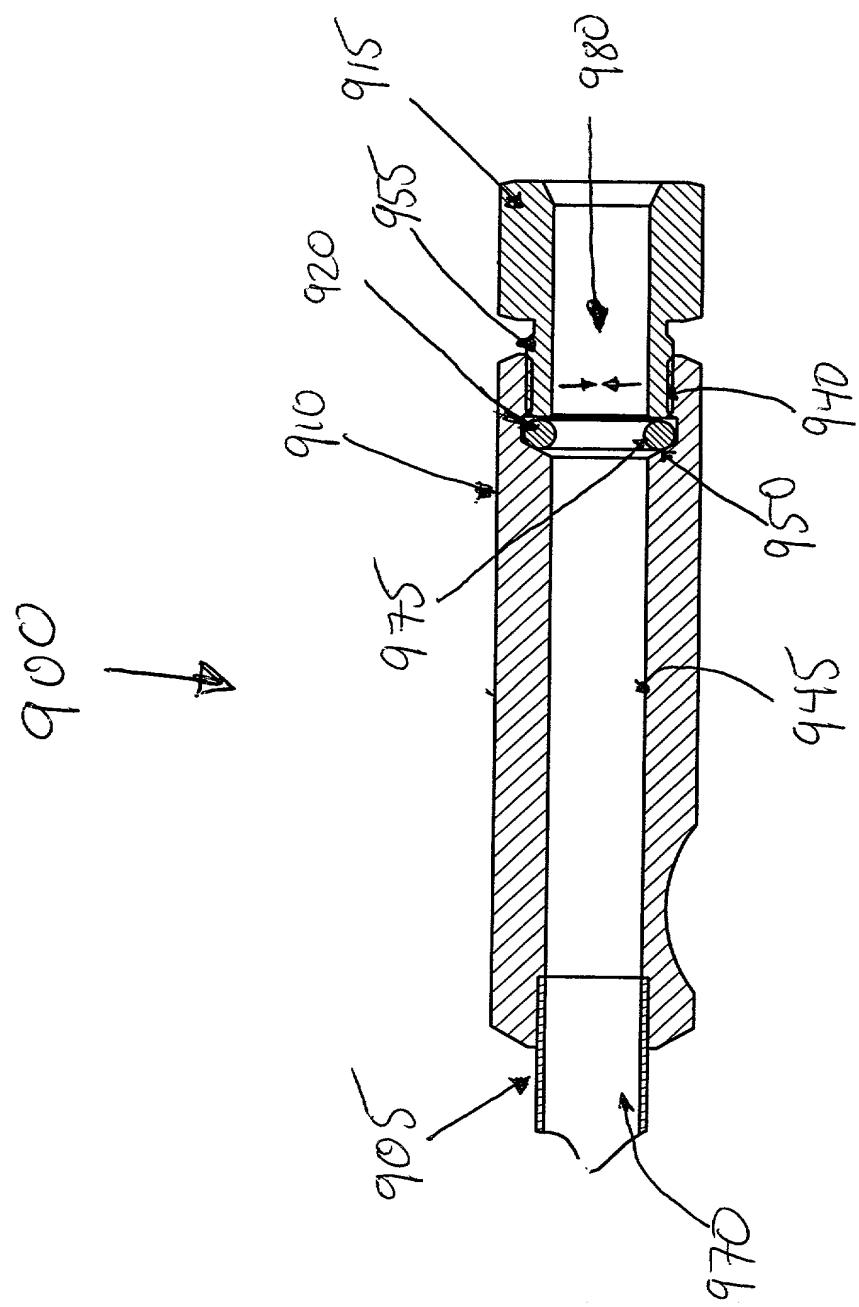


Figure 31

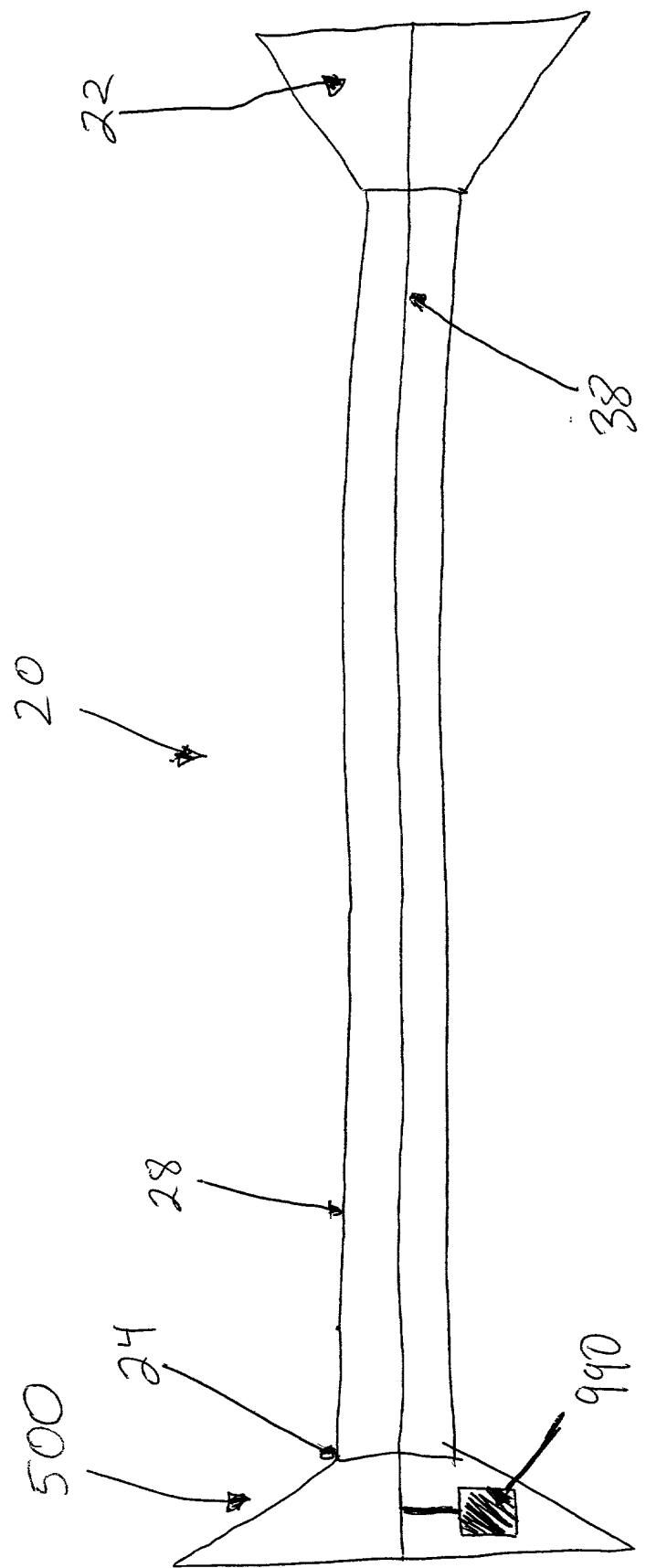


Figure 32

